



DESCRIPTION

The ABS1~ABS10 are available in ABS Package.

FEATURES

- Glass Passivated Chip Juntion
- Reverse Voltage 100 to 1000 V
- Forward Current 0.8 A
- High Surge Current Capability
- Designed for Surface Mount Application
- Available in ABS Package

ORDERING INFORMATION

Package Type	Part Number					
ABS	ABS1					
	ABS2					
	ABS4					
	ABS6					
	ABS8					
	ABS10					
Note	SPQ: 5,000pcs/Reel					
AiT provides all RoHS Compliant Products						

MECHANICAL DATA

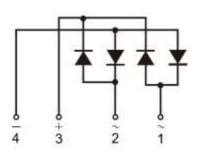
Case: ABS

Terminals: Solderable per MIL-STD-750,

Method 2026

Approx. Weight: 88mg 0.0029oz

PIN DESCRIPTION



- 1. Input Pin (~)
- 2. Input Pin (~)
- 3. Output Anode (+)
- 4. Output Cathode (-)

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MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified. Single phase half-wave 60 Hz, resistive or

inductive load, for capacitive load current derate by 20 %.

Parameter	Symbol	ABS1	ABS2	ABS4	ABS6	ABS8	ABS10	Unit	
Maximum Repetitive Pea	V _{RRM}	100	200	400	600	800	1000	V	
Maximum RMS Voltage					280	420	560	700	V
Maximum DC Blocking \	V _{DC}	100	200	400	600	800	1000	V	
Average Rectified Output at T _A =40°C	lo	0.8							
Peak Forward Surge Cu Single Half Sine Wave S on Rated Load (JEDEC	I _{FSM}	30							
Forward Voltage Per Element				1.0 1.1					
Maximum DC Reverse Current at Rated DC Blocking Voltage	T _A =25°C T _A =100°C T _A =125°C	I _R	5.0 100 500						uA
Typical Junction Capacita	Cj	13							
Typical Thermal Resista	Reja Rejl	80 16						°C /W	
Operating and Storage Range	T _J , T _{STG}	-55 ~ 150							

NOTE1: Measured at 1MHz and applied reverse voltage of 4 V D.C.

NOTE2: Mounted on glass epoxy PC board with 1.3mm² copper pad.

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TYPICAL CHARACTERISTICS

Figure 1. Average Rectified Output Current

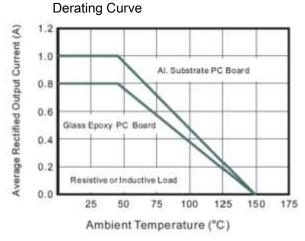


Figure 3. Typical Instantaneous Forward

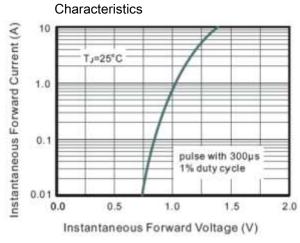


Figure 5. Maximum Non-Repetitive Peak Forward

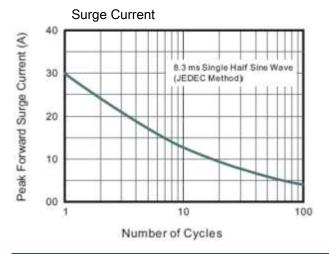


Figure 2. Typical Reverse Characteristics

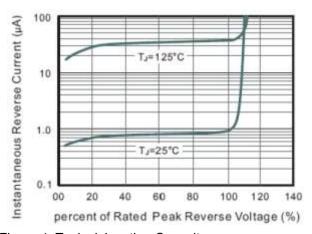
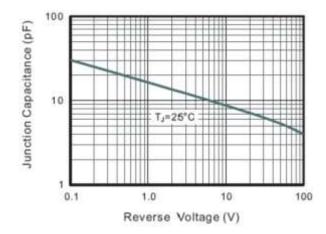


Figure 4. Typical Junction Capacitance

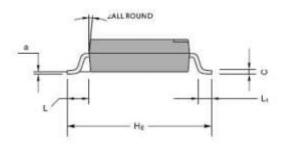


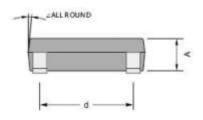
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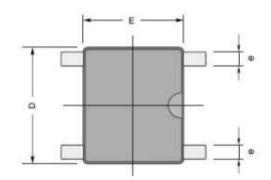


PACKAGE INFORMATION

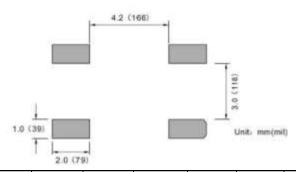
Dimension in ABS Package (Unit: mm/mil) Plastic surface mounted package; 4 leads







The recommended mounting pad size



	UNI	Т	Α	С	D	E	HE	d	е	L	L1	а	∠
	mm	Min	1.3	0.15	4.9	4.2	6.0	3.8	0.5	0.95	0.6	0.2	
mm	mm	Max	1.5	0.22	5.2	4.5	6.4	4.2	0.7				7 °
Ī	mil	Min	51	5.9	193	166	236	150	20	37	24	4	
mil	11111	Max	59	8.7	205	177	252	165	28				

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