



DESCRIPTION

AM3443C is available in a SOT-26 package.

ORDERING INFORMATION

Package Type	Part Number	
SOT-26 SPQ: 3,000pcs/Reel	E6	AM3443CE6R AM3443CE6VR
Note	V: Halogen free Package R: Tape & Reel	
AiT provides all RoHS products		

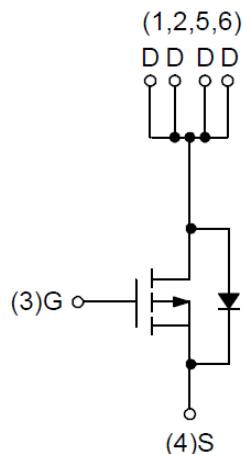
FEATURES

- -20V/-5.6A,
 $R_{DS(ON)} = 45m\Omega(\text{max.}) @ V_{GS}=-4.5V$
 $R_{DS(ON)} = 66m\Omega(\text{max.}) @ V_{GS}=-2.5V$
 $R_{DS(ON)} = 104m\Omega(\text{max.}) @ V_{GS}=-1.8V$
- 100% UIS + R_g Tested
- Reliable and Rugged
- Super High Dense Cell Design
- Available in SOT-26 Package

APPLICATION

- Power Management in Notebook Computer,
Portable Equipment and Battery Powered
Systems.

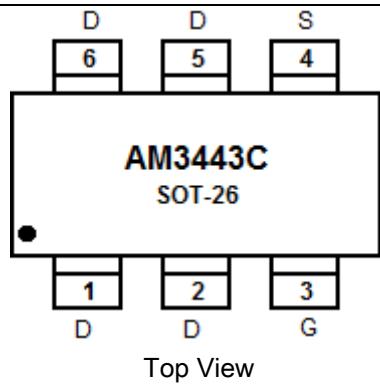
TYPICAL APPLICATION



P-Channel MOSFET



PIN DESCRIPTION



Top View

Pin #	Symbol	Function
1	D	Drain
2	D	Drain
3	G	Gate
4	S	Source
5	D	Drain
6	D	Drain



ABSOLUTE MAXIMUM RATINGS

$T_A = 25^\circ\text{C}$, unless otherwise noted

V_{DSS} , Drain-Source Voltage	-20V
V_{GSS} , Gate-Source Voltage	$\pm 12V$
I_D^* , Continuous Drain Current	$T_A=25^\circ\text{C}$ -5.6A
	$T_A=70^\circ\text{C}$ -4.4A
I_{DM}^* , 300 μs Pulsed Drain Current	$T_A=25^\circ\text{C}$ -22.2A
	$T_A=70^\circ\text{C}$ -17.8A
I_S^* , Diode Continuous Forward Current	-1A
T_J , Maximum Junction Temperature	150°C
T_{STG} , Storage Temperature Range	-55°C~150°C
P_D^* , Maximum Power Dissipation	$T_A=25^\circ\text{C}$ 2.1W
	$T_A=70^\circ\text{C}$ 1.3W
$R_{\theta JA}^*$, Thermal Resistance-Junction to Ambient	$t \leq 10\text{s}$ 60°C/W
	Steady State 100°C/W

Stress beyond above listed "Absolute Maximum Ratings" may lead permanent damage to the device. These are stress ratings only and operations of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

NOTE: *Surface Mounted on 1in² pad area, $t \leq 10\text{sec}$.



ELECTRICAL CHARACTERISTICS

$T_A = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{DS}}=-250\mu\text{A}$	-20	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=-16\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	-1	μA
		$\text{T}_J=85^\circ\text{C}$	-	-	-30	
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{th})}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_{\text{DS}}=-250\mu\text{A}$	-0.5	-0.7	-1	V
Gate Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm 12\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Drain-Source On-state Resistance	$\text{R}_{\text{DS}(\text{ON})}$ NOTE1	$\text{V}_{\text{GS}}=-4.5\text{V}, \text{I}_{\text{DS}}=-5.6\text{A}$	-	36	45	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=-2.5\text{V}, \text{I}_{\text{DS}}=-3.5\text{A}$	-	49	66	
		$\text{V}_{\text{GS}}=-1.8\text{V}, \text{I}_{\text{DS}}=-1.5\text{A}$	-	72	104	
Diode Forward Voltage	V_{SD} NOTE1	$\text{I}_{\text{SD}}=-1\text{A}, \text{V}_{\text{GS}}=0\text{V}$	-	-0.7	-1	V
Gate Charge Characteristics NOTE2						
Total Gate Charge	Q_g	$\text{V}_{\text{DS}}=-10\text{V}, \text{V}_{\text{GS}}=-4.5\text{V},$ $\text{I}_{\text{DS}}=-5.6\text{A}$	-	6.8	-	nC
Gate-Source Charge	Q_{gs}		-	0.8	-	
Gate-Drain Charge	Q_{gd}		-	2.5	-	
Dynamic Characteristics NOTE2						
Gate Resistance	R_g	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=0\text{V}, f=1\text{MHz}$	-	3.6	-	Ω
Input Capacitance	C_{iss}	$\text{V}_{\text{GS}}=0\text{V},$ $\text{V}_{\text{DS}}=-10\text{V},$ Frequency=1.0MHz	-	590	-	pF
Output Capacitance	C_{oss}		-	122	-	
Reverse Transfer Capacitance	C_{rss}		-	92	-	
Turn-on Delay Time	$\text{t}_{\text{d}(\text{ON})}$	$\text{V}_{\text{DD}}=-10\text{V}, \text{R}_L=10\Omega,$ $\text{I}_{\text{DS}}=-1\text{A}, \text{V}_{\text{GEN}}=-4.5\text{V},$ $\text{R}_G=6\Omega$	-	7.2	-	ns
Turn-on Rise Time	t_r		-	13.4	-	
Turn-off Delay Time	$\text{t}_{\text{d}(\text{OFF})}$		-	26	-	
Turn-off Fall Time	t_f		-	17	-	
Reverse Recovery Time	t_{rr}	$\text{I}_{\text{SD}}=-5.6\text{A},$ $d\text{I}_{\text{SD}}/dt=100\text{A}/\mu\text{s}$	-	18	-	ns
Reverse Recovery Charge	Q_{rr}		-	7	-	nC

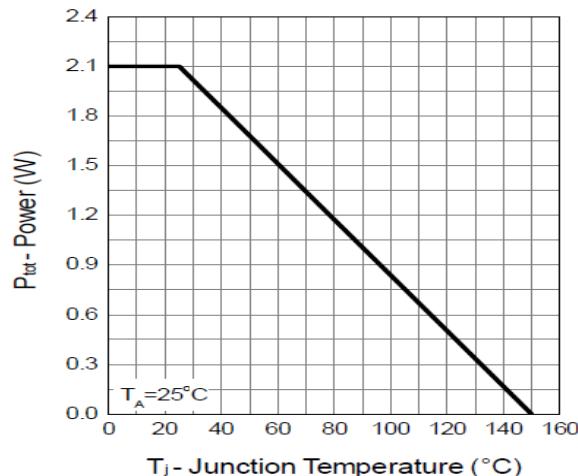
NOTE1: Pulse test; pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

NOTE2: Guaranteed by design, not subject to production testing.

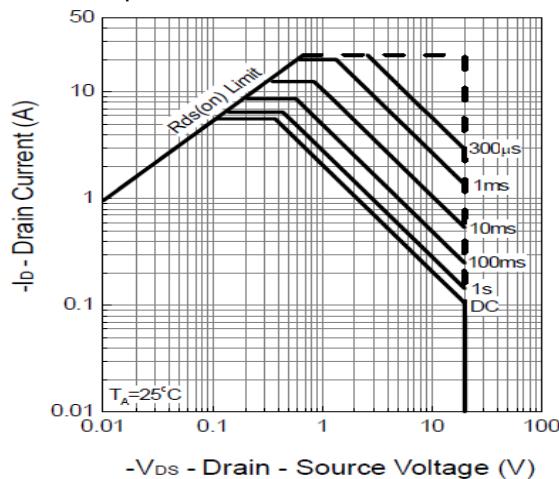


TYPICAL PERFORMANCE CHARACTERISTICS

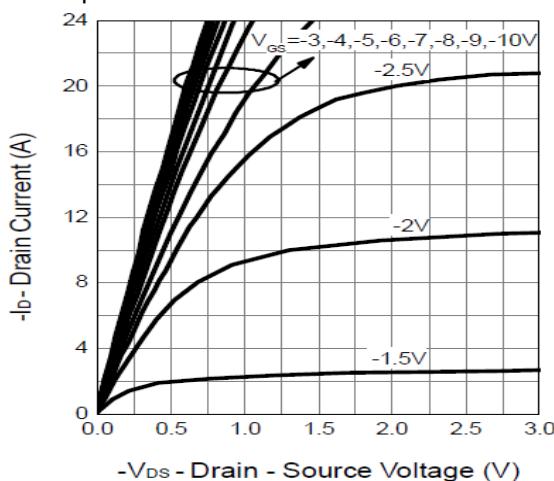
1. Power Dissipation



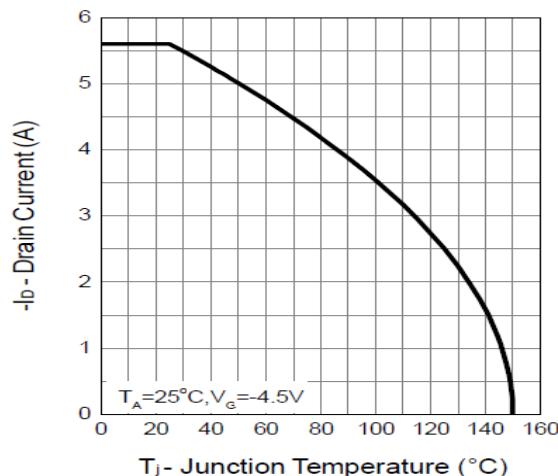
3. Safe Operation Area



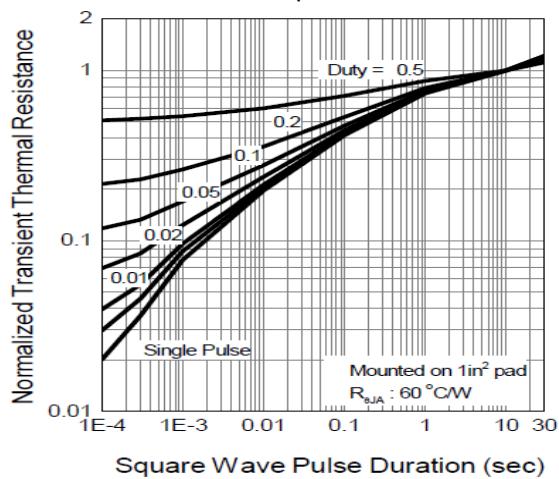
5. Output Characteristics



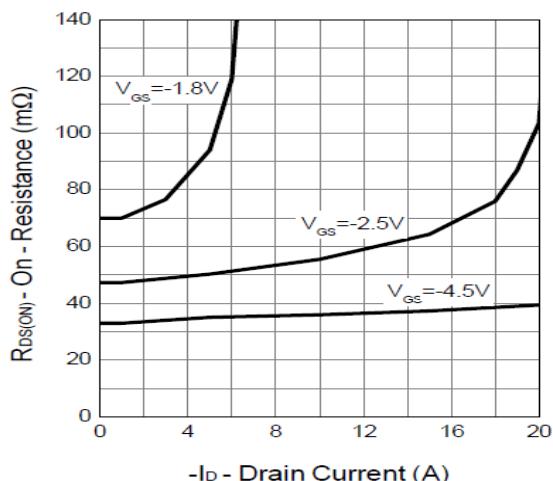
2. Drain Current



4. Thermal Transient Impedance

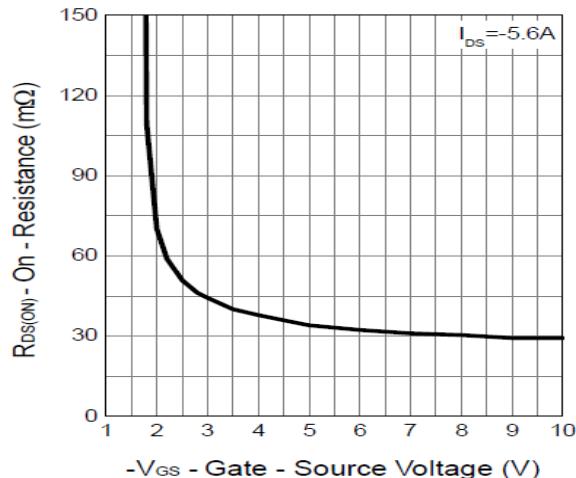


6. Drain-Source On Resistance

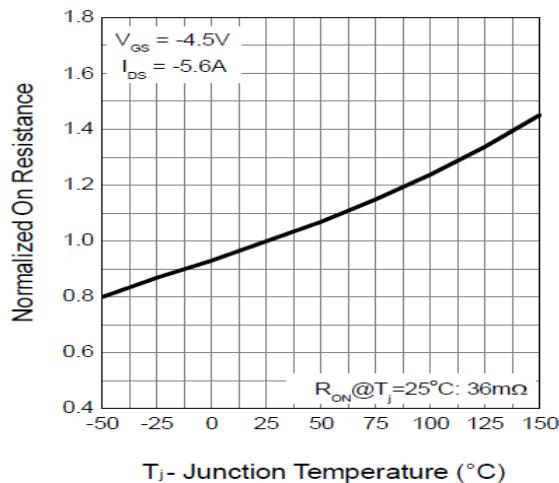




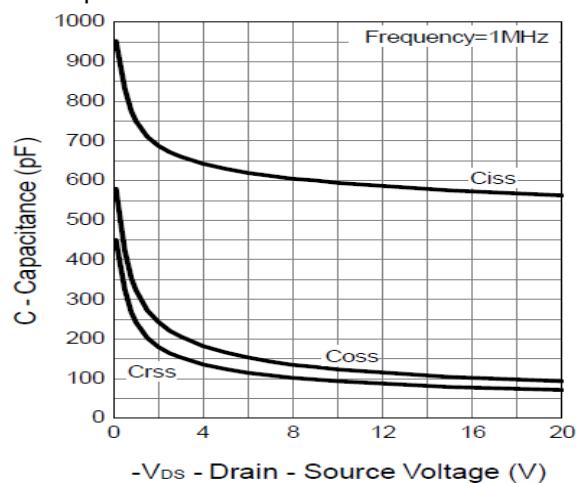
7. Gate-Source On Resistance



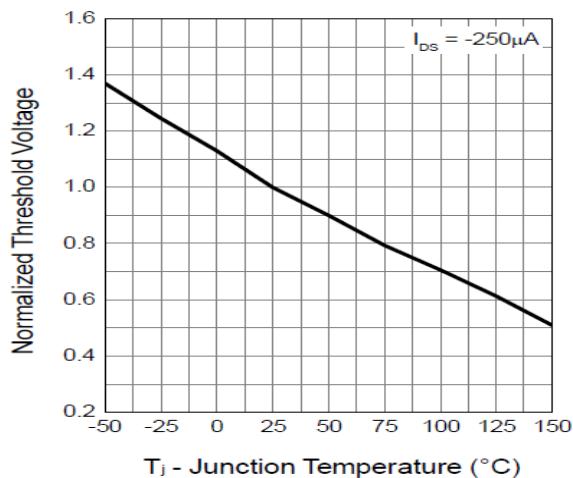
9. Drain-Source On Resistance



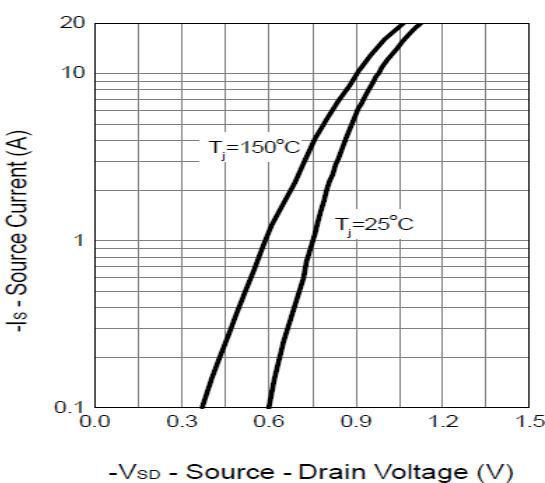
11. Capacitance



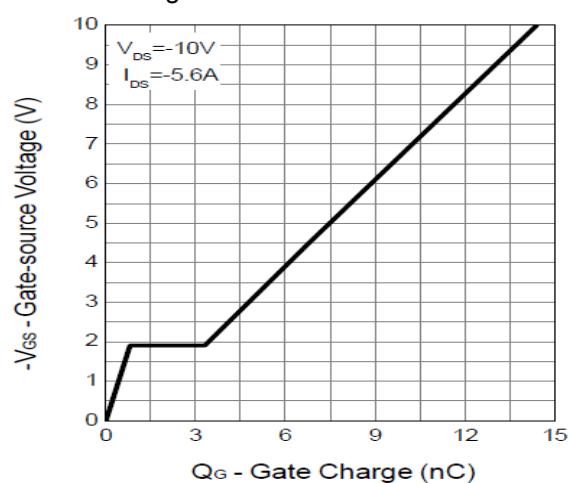
8. Gate Threshold Voltage



10. Source-Drain Diode Forward



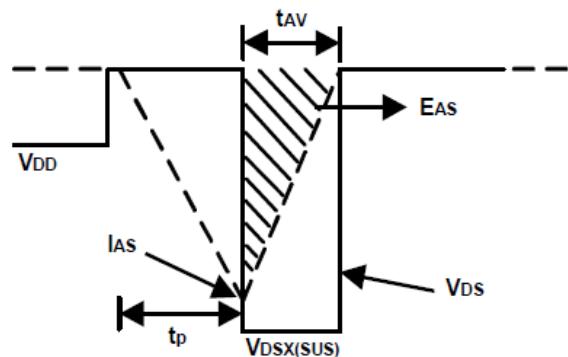
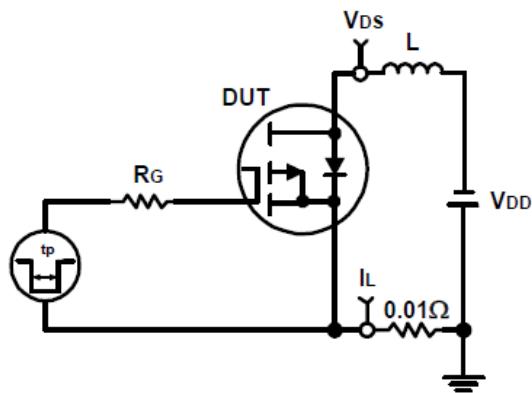
12. Gate Charge



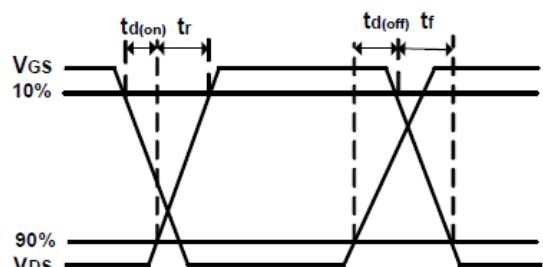
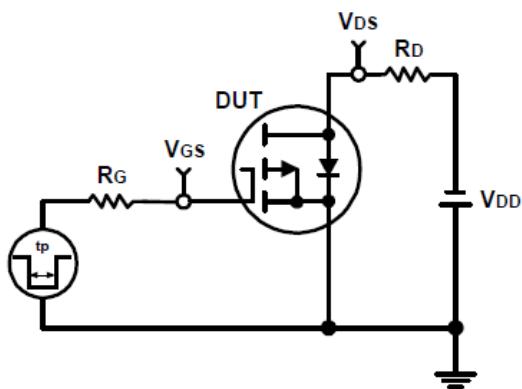


DETAILED INFORMATION

1. Avalanche Test Circuit and Waveforms



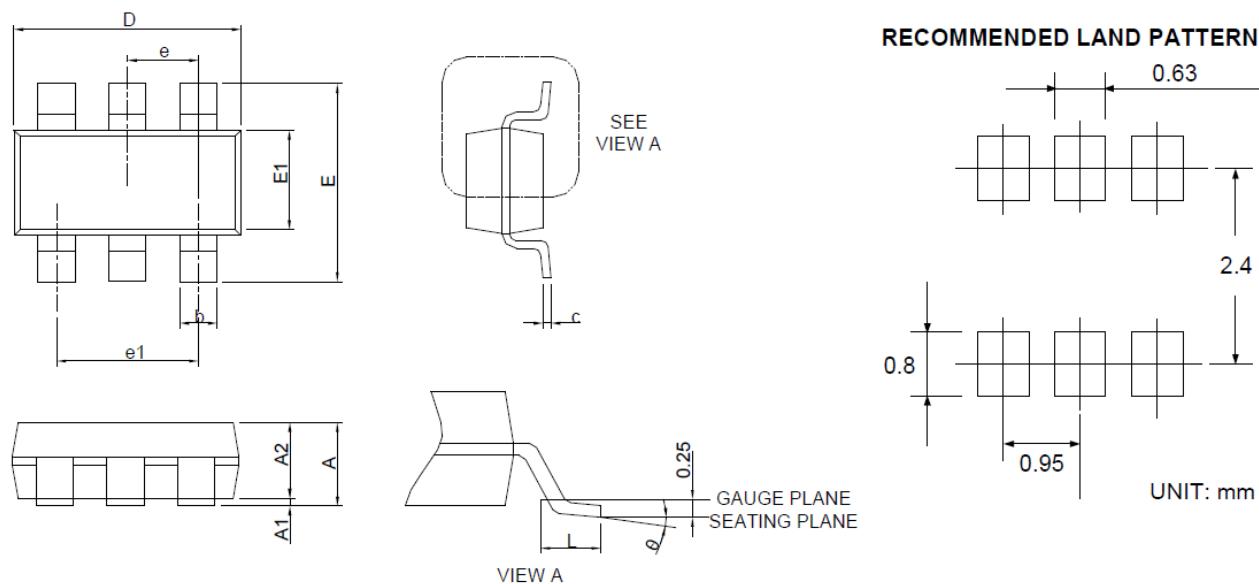
2. Switching Time Test Circuit and Waveforms





PACKAGE INFORMATION

Dimension in SOT-26 Package (Unit: mm)



Symbol	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	-	1.25	-	0.049
A1	0.00	0.05	0.000	0.002
A1	0.90	1.20	0.035	0.047
b	0.30	0.50	0.012	0.020
c	0.08	0.22	0.003	0.009
D	2.70	3.10	0.106	0.122
E	2.60	3.00	0.102	0.118
E1	1.40	1.80	0.055	0.071
e	0.95 BSC		0.037 BSC	
e1	1.90 BSC		0.075 BSC	
L	0.30	0.60	0.012	0.024
θ	0°	8°	0°	8°



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