



DESCRIPTION

The AM6260 is available in DFN8(5x6) package.

ORDERING INFORMATION

Package Type	Part Number	
DFN8(5x6) SPQ: 4,000pcs/Reel	J8	AM6260J8R
		AM6260J8VR
Note	V: Halogen free Package R: Tape & Reel	
AiT provides all RoHS products		

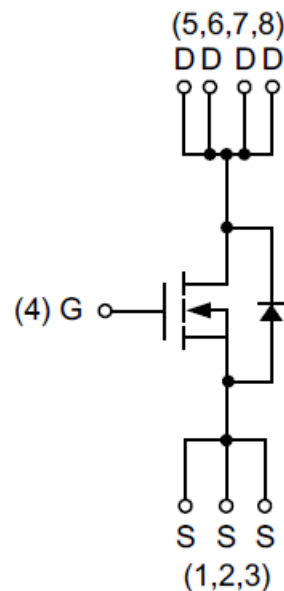
FEATURES

- 60V/100A
 $R_{DS(ON)}=2.3m\Omega(max.)@V_{GS}=10V$
 $R_{DS(ON)}=3.4m\Omega(max.)@V_{GS}=4.5V$
- 100% UIS + R_g Tested
- Reliable and Rugged
- Available in DFN8(5x6) Package

APPLICATIONS

- Secondary Side Synchronous Rectification.
- DC-DC Converter.
- Motor Control.
- Load Switching.

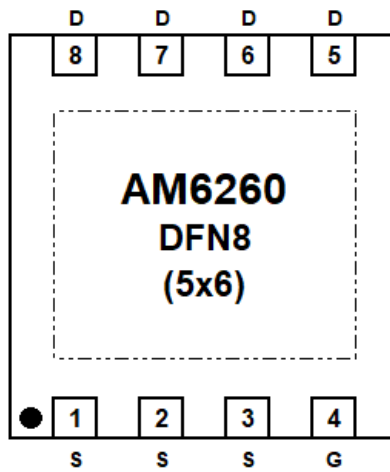
TYPICAL APPLICATION



N-Channel MOSFET



PIN DESCRIPTION



Top View

Pin #	Symbol	Function
1	S	Source
2	S	Source
3	S	Source
4	G	Gate
5	D	Drain
6	D	Drain
7	D	Drain
8	D	Drain



ABSOLUTE MAXIMUM RATINGS

T_A = 25°C, unless otherwise noted

V _{DSS} , Drain-Source Voltage		60V
V _{GSS} , Gate-Source Voltage		±20V
I _S , Diode Continuous Forward Current	T _C = 25°C	100A ^{NOTE1}
I _D , Continuous Drain Current	T _C = 25°C	100A ^{NOTE1}
	T _C = 100°C	100A ^{NOTE1}
I _{DM} , Pulsed Drain Current	T _C = 25°C	400A ^{NOTE2}
P _D , Maximum Power Dissipation	T _C = 25°C	250W
	T _C = 100°C	100W
R _{θJC} , Thermal Resistance-Junction to Case	Steady State	0.5°C/W
I _D , Continuous Drain Current	T _A = 25°C	21A
	T _A = 70°C	17A
P _D , Maximum Power Dissipation	T _A = 25°C	2.08W
	T _A = 70°C	1.33W
R _{θJA} , Thermal Resistance-Junction to Ambient ^{NOTE3}	Steady State	60°C/W
I _{AS} , Avalanche Current, Single pulse ^{NOTE4}	L=0.5mH	36A
E _{AS} , Avalanche Energy, Single pulse ^{NOTE4}	L=0.5mH	324mJ
T _J , Maximum Junction Temperature		150°C
T _{STG} , Storage Temperature Range		-55°C ~ 150°C

Stress beyond above listed "Absolute Maximum Ratings" may lead permanent damage to the device. These are stress ratings only and operations of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



ELECTRICAL CHARACTERISTICS

T_A = 25°C, unless otherwise noted

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _{DS} =250μA	60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =48V, V _{GS} =0V T _J =85°C	-	-	1	μA
			-	-	30	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _{DS} =250μA	1	2	3	V
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V	-	-	±100	nA
Drain-source On-Resistance ^{NOTE5}	R _{D(S)ON}	V _{GS} =10V, I _{DS} =25A	-	1.9	2.3	mΩ
		V _{GS} =4.5V, I _{DS} =25A	-	2.6	3.4	
Diode Characteristics						
Diode Forward Voltage ^{NOTE5}	V _{SD}	I _{SD} =20A, V _{GS} =0V	-	0.8	1.3	V
Reverse Recovery Time	t _{rr}	I _{SD} =25A,	-	50	-	ns
Reverse Recovery Charge	Q _{rr}	di _{SD} /dt=100A/μs	-	72	-	nC
Dynamic Characteristics^{NOTE6}						
Gate Resistance	R _G	V _{GS} =0V, V _{DS} =0V, f=1MHz	-	1.0	-	Ω
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =30V, f=1MHz	-	4950	6435	pF
Output Capacitance	C _{oss}		-	1000	-	
Reverse Transfer Capacitance	C _{rss}		-	130	-	
Turn-on Delay Time	t _{d(on)}	V _{DD} =30V, R _L =30Ω, I _{DS} =1A, V _{GEN} =10V, R _G =6Ω	-	25	45	ns
Turn-on Rise Time	t _r		-	12	22	
Turn-off Delay Time	t _{d(off)}		-	90	162	
Turn-off Fall Time	t _f		-	100	180	
Gate Charge Characteristics^{NOTE6}						
Total Gate Charge	Q _g	V _{DS} =30V, V _{GS} =4.5V, I _{DS} =25A	-	41	-	nC
Total Gate Charge	Q _g	V _{DS} =30V, V _{GS} =10V, I _{DS} =25A	-	87	122	
Gate-Source Charge	Q _{gs}		-	18	-	
Gate-Drain Charge	Q _{gd}		-	14	-	

NOTE1: Current limited by bonding wire.

NOTE2: Pulse width limited by max. junction temperature.

NOTE3: Surface Mounted on 1in² pad area.

NOTE4: UIS tested and pulse width limited by maximum junction temperature 150oC (initial temperature T_J=25°C).

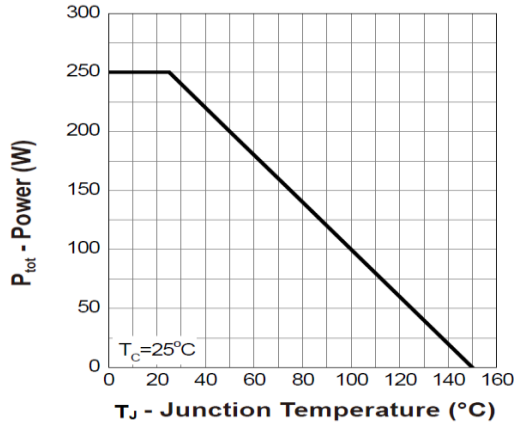
NOTE5: Pulse test ; pulse width≤300ms, duty cycle≤2%.

NOTE6: Guaranteed by design, not subject to production testing.

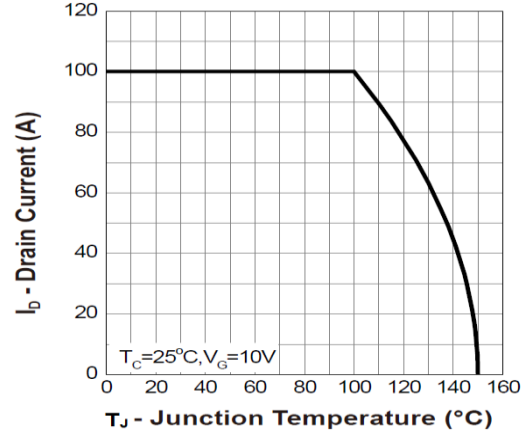


TYPICAL ELECTRICAL CHARACTERISTICS

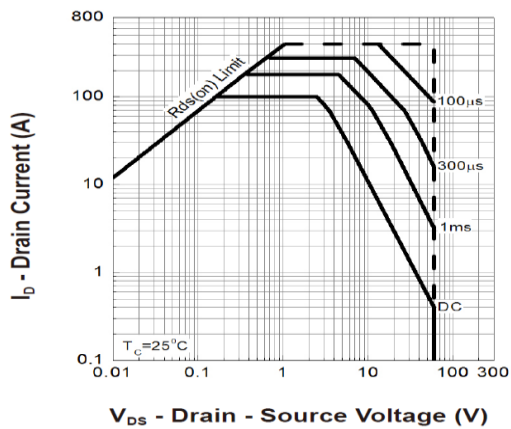
1. Power Dissipation



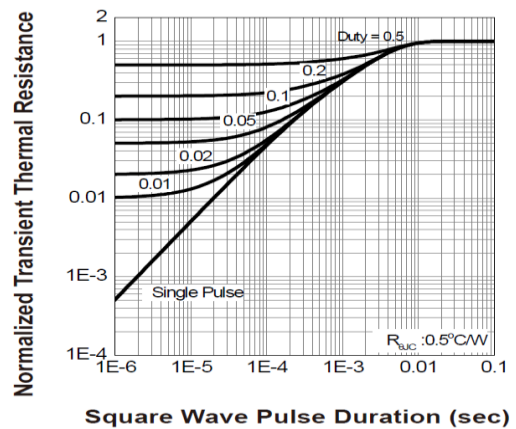
2. Drain Current



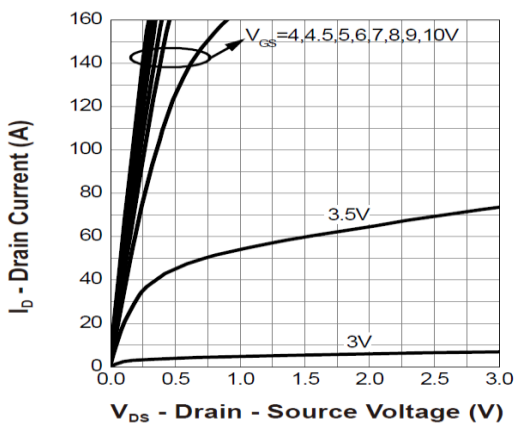
3. Safe Operation Area



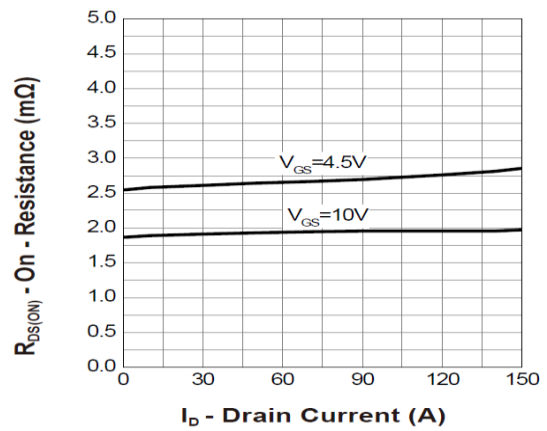
4. Thermal Transient Impedance



5. Output Characteristics

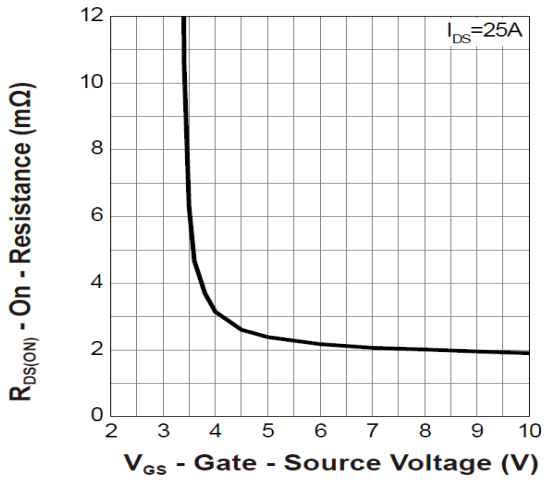


6. Drain-Source On Resistance

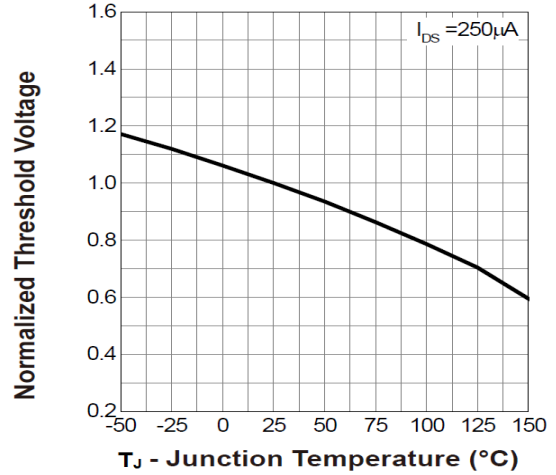




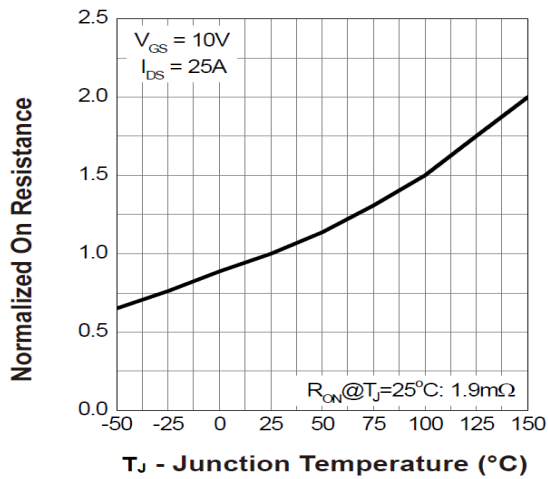
7. Gate-Source On Resistance



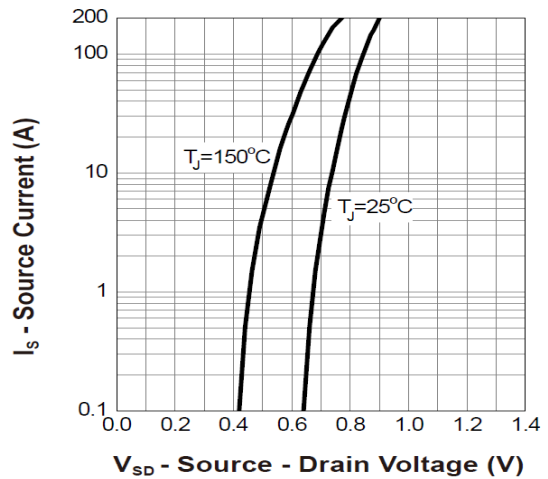
8. Gate Threshold Voltage



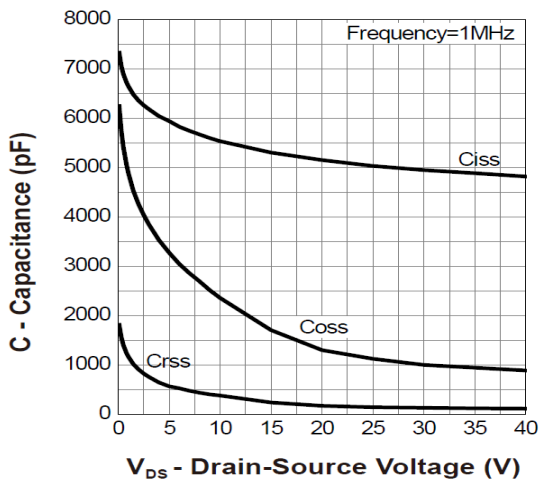
9. Drain-Source On Resistance



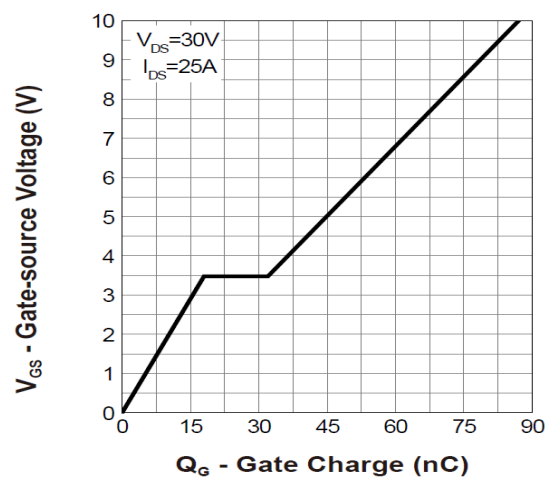
10. Source-Drain Diode Forward



11. Capacitance

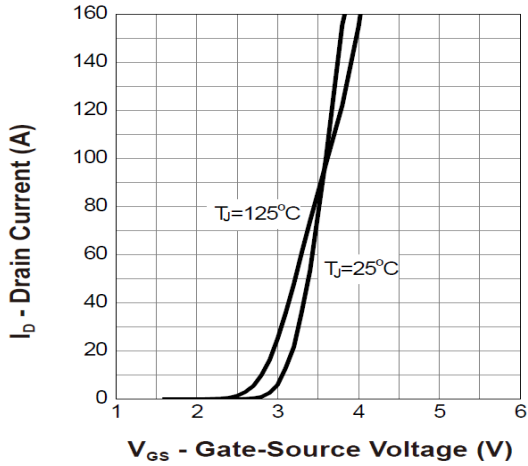


12. Gate Charge

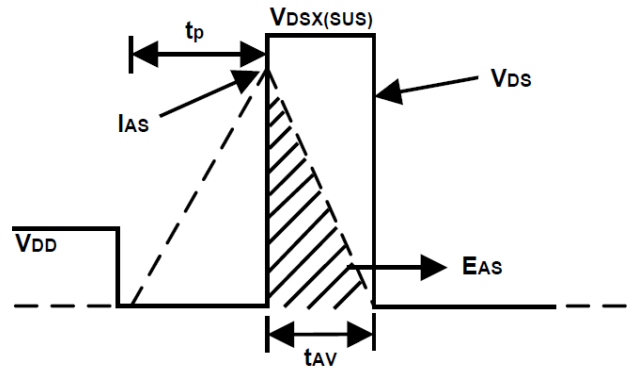
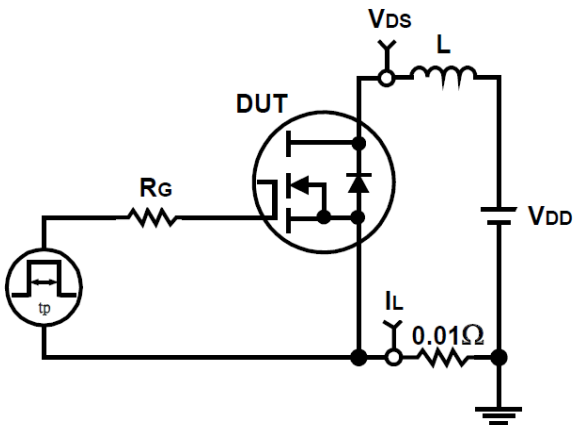




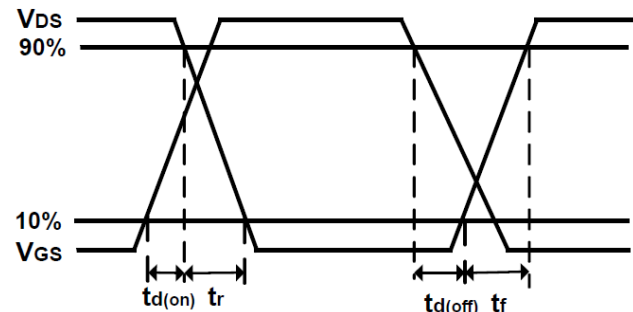
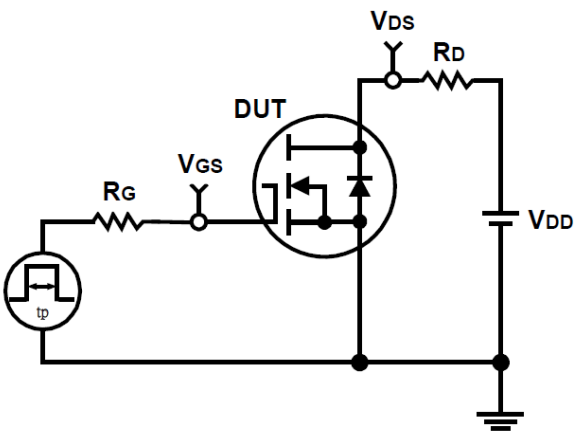
13. Transfer Characteristics



Avalanche Test Circuit and Waveforms



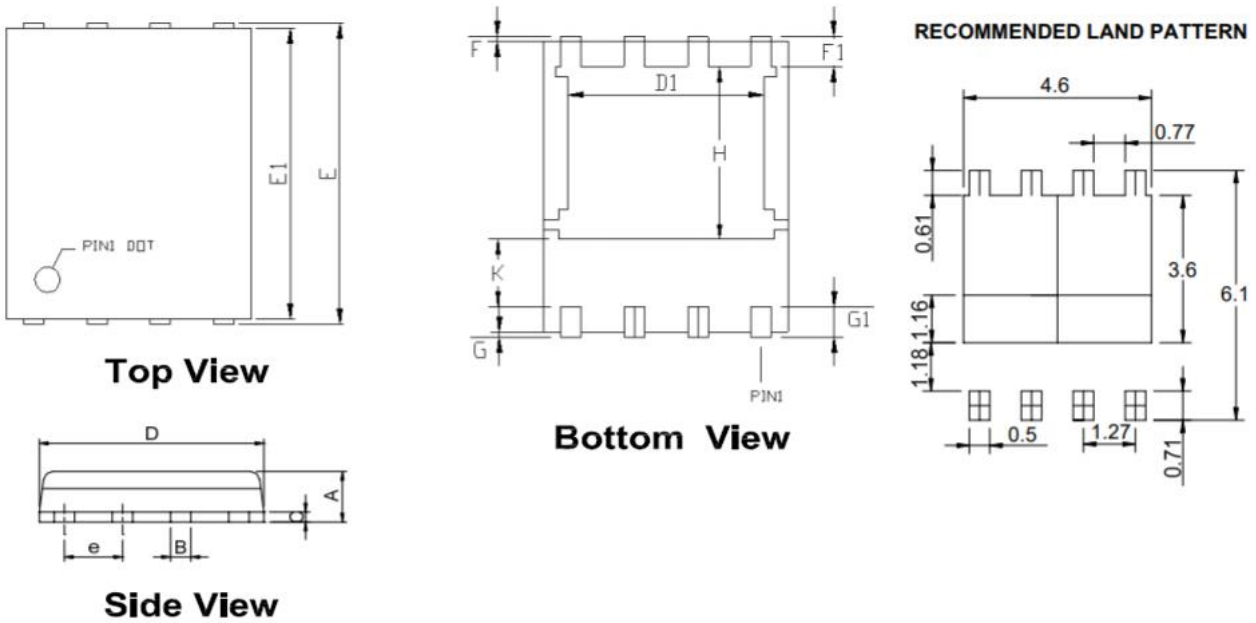
Switching Time Test Circuit and Waveforms





PACKAGE INFORMATION

Dimension in DFN8 (Unit: mm)



Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	0.90	1.20	0.035	0.047
B	0.30	0.51	0.012	0.020
C	0.19	0.25	0.007	0.010
D	4.80	5.30	0.189	0.209
D1	3.60	4.40	0.141	0.173
E	5.90	6.20	0.232	0.244
E1	5.50	5.80	0.217	0.228
e	1.27BSC		0.050BSC	
F	0.05	0.30	0.002	0.012
F1	0.35	0.75	0.014	0.030
G	0.05	0.30	0.002	0.012
G1	0.35	0.75	0.014	0.030
H	3.34	3.90	0.131	0.154
K	0.762	-	0.030	-



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