DESCRIPTION

The AP8836 consists of a Low Standby-Power Quasi-Resonant (QR) Primary-Side controller and a 650V avalanche-rugged smart power VDMOSFET, specifically designed for a high performance AC/DC charger or adaptor with minimal external components.

AP8836 operates in primary-side sensing and regulation, so opto-coupler and A431 could be eliminated. Because of internal HV Start-up circuit, the system with AP8836 can achieve less than 50mW standby power consumption (264VAC). In CV mode, multi-mode and quasi resonant technique is utilized to achieve high efficiency, avoid audible noise and make the system meeting Energy star class VI. Good load regulation is achieved by the built-in cable drop compensation. In CC mode, the current and output power setting can be adjusted externally by the sense resistor at CS pin. AP8836 offers complete protections including Cycle-by-Cycle current limiting protection (OCP), over voltage protection (OVP), over temperature protection (OTP) and CS open or short protection (CSO/SP) etc.

The AP8836 is available in DIP8 and SOP8 packages.

ORDERING INFORMATION

<table>
<tr><th>Package Type</th><th colspan="2">Part Number</th></tr>
<tr><td rowspan="2">DIP8
SPQ: 50pcs/Tube</td><td rowspan="2">P8</td><td>AP8836P8U</td></tr>
<tr><td>AP8836P8VU</td></tr>
<tr><td rowspan="2">SOP8
SPQ: 4,000pcs/Reel</td><td rowspan="2">M8</td><td>AP8836M8R</td></tr>
<tr><td>AP8836M8VR</td></tr>
<tr><td>Note</td><td colspan="2">V: Halogen free Package
R: Tape & Reel
U: Tube</td></tr>
<tr><td colspan="3">AiT provides all RoHS products</td></tr>
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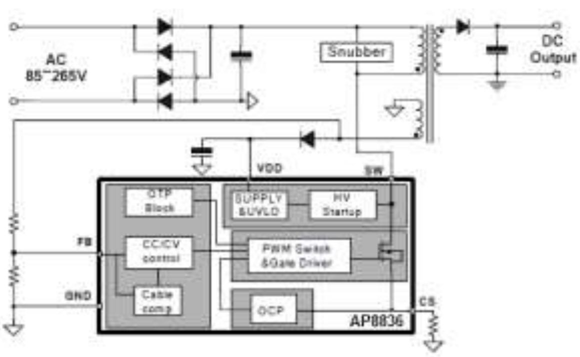
FEATURES

- Internal 650 V avalanche-rugged smart power VDMOSFET
- Internal HV Start-up Circuit, Standby power consumption < 50mW at 264VAC
- Multi-mode and Quasi-Resonant technique
- ±5% CC Regulation at Universal AC input
- Primary-side Sensing and Regulation without A431 and Opto-coupler
- Programmable Cable Drop Compensation
- No-need Control Loop Compensation Capacitance
- Excellent Protection Coverage:
  Over Temperature Protection (OTP)
  V_{DD} Under/Over Voltage Protection(UVLO&OVP)
  Cycle-by-Cycle Current Limiting (OCP)
  Cs Short/Open Protection (CS O/SP)
- Available in DIP8 and SOP8 Packages

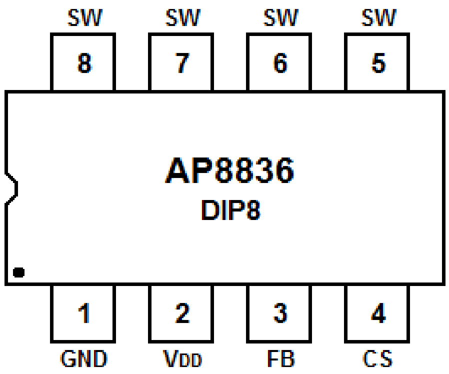
APPLICATION

- Switch AC/DC Adaptor
- Battery Charger

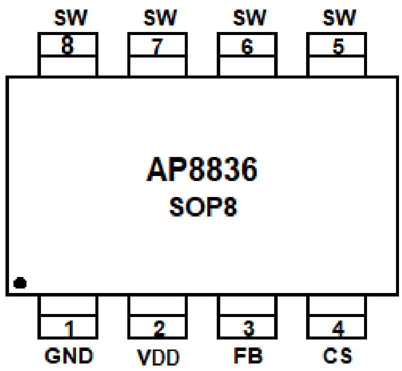
TYPICAL APPLICATION



PIN DESCRIPTION



Top View



Top View

Pin #	Symbol	Function
1	GND	Ground
2	V_{DD}	Power supply
3	FB	The voltage feedback from auxiliary winding. Connected to resistor divider from auxiliary winding reflecting output voltage.
4	CS	Current Sense Input
5,6,7,8	SW	Avalanche-rugged power MOSFET Drain pin. The Drain pin is connected to the primary lead of the transformer.

TYPICAL POWER

Part number	Package	Adapter NOTE1
		85-265 V_{AC}
AP8836	DIP8	18W
AP8836	SOP8	15W

NOTE1: Maximum output power is tested in an adapter at 45°C ambient temperature, with enough cooling conditions.

ABSOLUTE MAXIMUM RATINGS

Supply Voltage Pin V_{DD}		-0.3V ~ 40V
High-Voltage Pin, SW		-0.3V ~ 650V
Pin FB, CS		-0.3V ~ 5.5V
Operating Junction Temperature		-40℃ ~ 150℃
Storage Temperature Range		-55℃ ~ 150℃
$R_{\theta JC}$, Package Thermal Resistance	DIP8	40°C/W
$R_{\theta JC}$, Package Thermal Resistance	SOP8	80°C/W
Lead Temperature (Soldering, 10secs)		260℃
HBM ESD Protection (ESDA/JEDEC JDS-001-2014)		±4.0kV
Pulse Drain Current (t_{pulse}=100us)		5A

Stress beyond above listed “Absolute Maximum Ratings” may lead permanent damage to the device. These are stress ratings only and operations of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

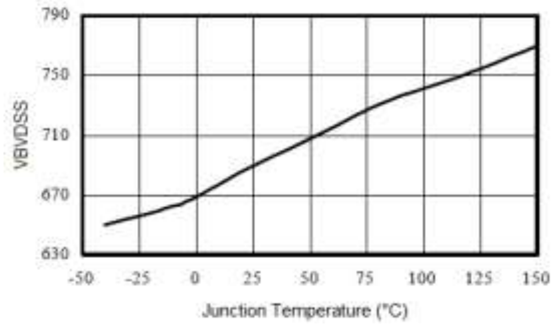
T_A =25°C, V_{DD} = 21V, unless otherwise specified

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Power section						
Break-Down Voltage	BV_{DSS}	I_{SW} = 250uA	650	700	-	V
Off-State Drain Current	I_{OFF}	V_{SW} = 500V	-	-	100	uA
Drain-Source On State Resistance	$R_{DS(ON)}$	I_{SW} = 1A,T_J = 25℃	-	1.6	-	Ω
Start up Threshold	V_{SW_START}	V_{DD}=V_{DDon} - 1V	-	30	-	V
Supply Voltage Section						
Operating Voltage Range	V_{DD}		10	-	30	V
V_{DD} Start up Threshold	V_{DDon}		14.5	16.5	18.5	V
V_{DD} Under Voltage Shutdown Threshold	V_{DDoff}		7.5	8.5	9.5	V
V_{DD} Over Voltage Protect	V_{DD_OVP}		30	34	38	V
Supply Current Section						
V_{DD} Charge Current	I_{DD_CH}	V_{DD}=V_{DDon} - 1V, V_{SW}=100V	-	0.85	-	mA
Operating Current, Switching	I_{DD}	V_{DD} = 19.5V	0.3	0.5	0.7	mA
Operating Current After Fault	I_{DD_FAULT}	V_{DD} = 15V after fault	-	0.5	-	mA

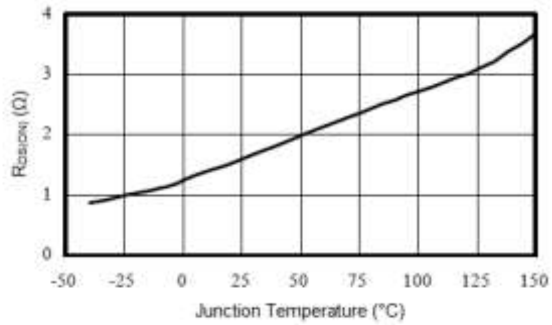
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Current Sense Section						
Current Sense Threshold	V_{TH_OC}		485	500	515	mV
Maximum Current Sense Threshold	$V_{TH\_OC\_MAX}$		-	560	-	mV
Minimum CS Threshold	V_{cs_min}		-	170	-	mV
Leading Edge Blanking Time	t_{LEB}		-	300	-	ns
Maximum ton	t_{onmax}		-	50	-	us
OCP Propagation Delay	t_{D_OC}		-	100	-	ns
FB Section						
Reference Voltage For Feedback Threshold	V_{REF_CV}		2.475	2.5	2.525	V
Output Over Voltage Protection Threshold	V_{FBOVP}		2.85	3	3.15	V
Output Under Voltage Threshold	V_{UVP}		-	1.55	-	V
Maximum Cable Compensation Current	I_{cable}	V_{FB}=0V	44	48	52	uA
Minimum toff	t_{offmin}		-	5	-	us
Maximum toff	t_{offmax}		-	2.2	-	ms
Output under Voltage Protection Blanking Time	t_{UVP}	f_{SW}= 50kHz	20	-	32	ms
Thermal Section						
Thermal Shutdown Temperature Threshold	T_{SD}		135	150	-	°C
Thermal Shutdown Hysteresis	T_{HYST}		-	30	-	°C

TYPICAL PERFORMANCE CHARACTERISTICS

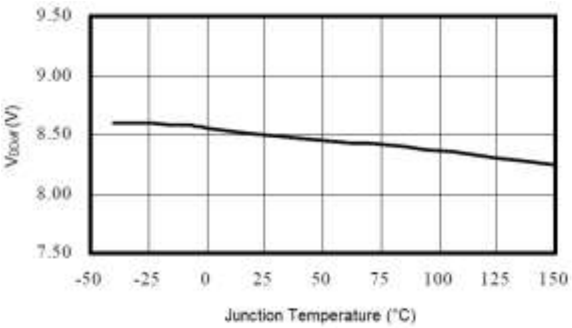
1. BV vs. T_J



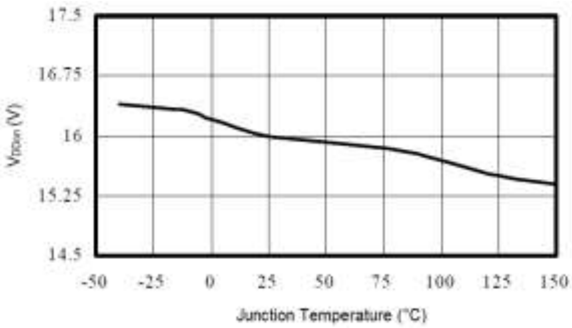
2. R_{DSON} vs. T_J



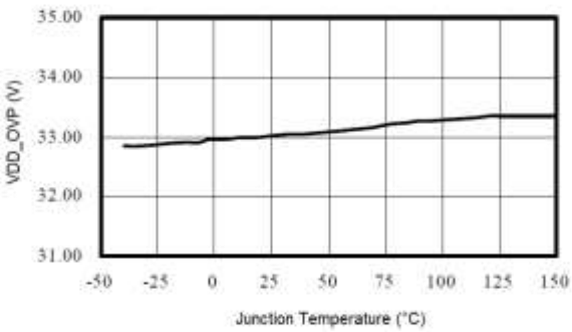
3. V_{DDoff} vs. T_J



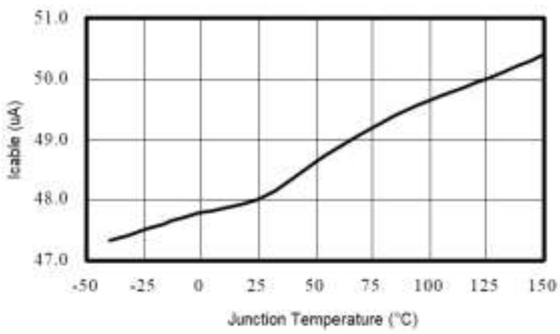
4. V_{DDon} vs. T_J



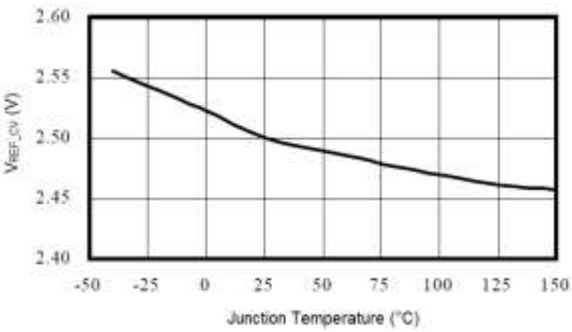
5. V_{DD_OVP} vs. T_J



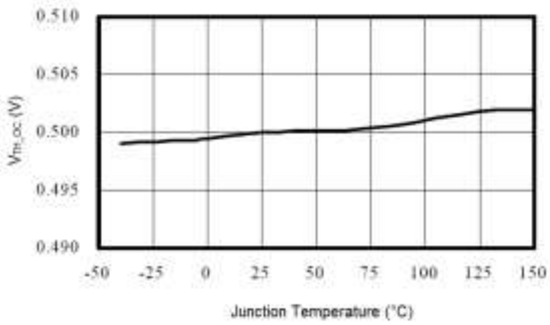
6. Icable vs. T_J



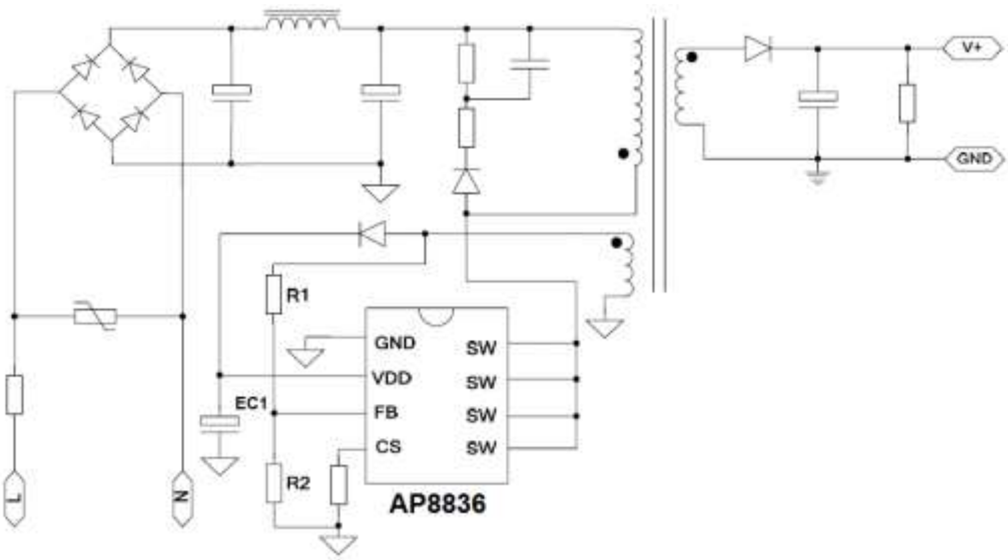
7. V_{REF_CV} vs. T_J



8. V_{TH_OC} vs. T_J



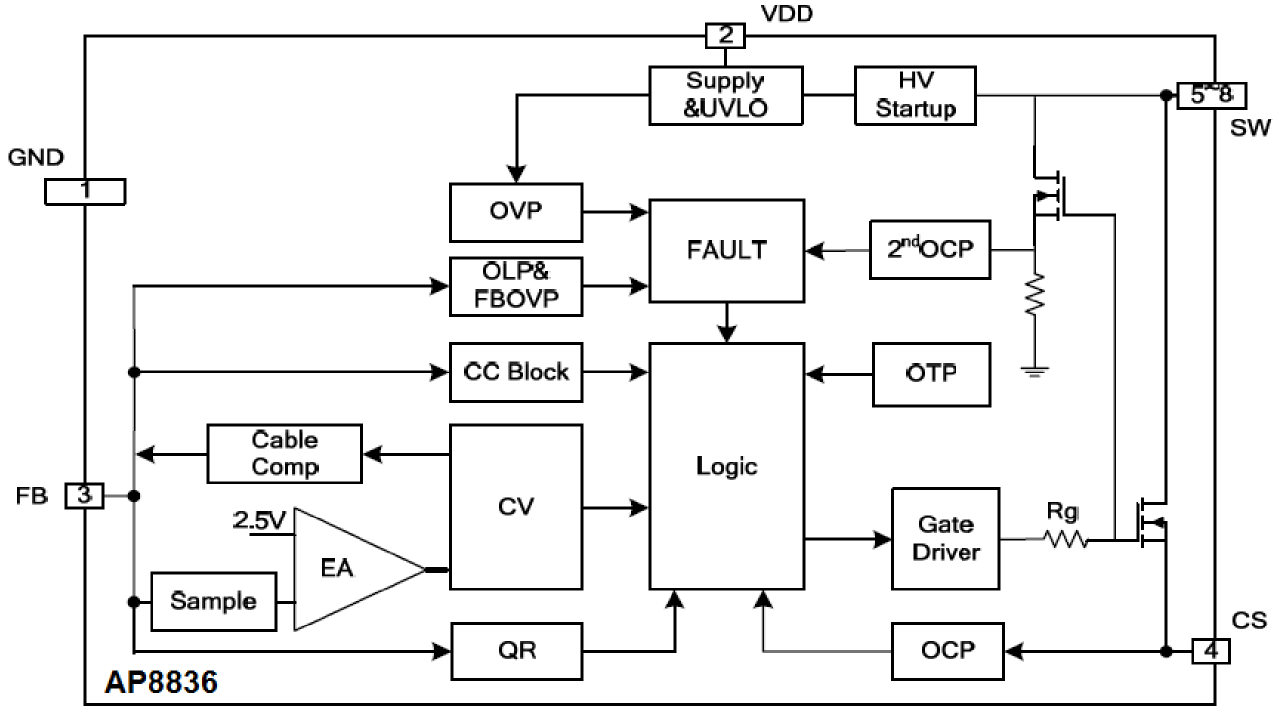
TYPICAL CIRCUIT



Component Parameter and Layout Considerations:

1. V_{DD} capacitor EC1 should be placed at the nearest place from the V_{DD} pin and the GND pin.

BLOCK DIAGRAM



DETAILED INFORMATION

Functional Description

The AP8836 is a high performance CC/CV primary-side controller. AP8836 operates in primary-side sensing and regulation, so opto-coupler and A431 could be eliminated. Proprietary built-in CV and CC control can achieve high precision CC/CV control meeting most charger and adaptor application requirements. Internal HV Start-up circuit and the chip's low consumption help the system to meet strict standby power standard.

HV Start up Control

At start up, the internal high-voltage start-up circuit provides the internal bias and charges the external V_{DD} capacitor, so that AP8836 starts up quickly. When V_{DD} reaches V_{DDon}, the device starts switching and the internal high-voltage current source stops charging the capacitor. The device keeps in normal operation provided as long as V_{DD} keeps above V_{DDoff}. After startup, the bias is supplied from the auxiliary transformer winding, the current of HV start-up circuit is designed to be very low so that the power consumption is very low.

CC Operation Mode

In CC operation mode, the AP8836 captures the auxiliary flyback signal at FB pin through a resistor dividing-network.

The pulse width of the auxiliary flyback signal determines the AP8836 oscillator frequency. The higher the output voltage is, the shorter the pulse width is, and the higher the chip oscillator frequency is, thus the constant output current can be achieved.

The current waveform of DCM mode is shown in Figure 1. During MOSFET turn-on time, the current in the primary winding (Ipri) ramps up. When MOSFET turns off, the energy stored in the primary winding is transferred to the secondary side, so the peak current in the secondary winding is

$$I_{sec\_pk} = I_{pri\_pk} \times N_{ps} \qquad (1)$$

The output current is

$$I_O = \frac{I_{sec\_pk}}{2} \times \frac{T_{demag}}{T_p} = \frac{1}{2} N_{ps} \frac{V_{cs}}{R_{sense}} \frac{T_{demag}}{T_p} \qquad (2)$$

Because V_{CS} is constant and T_p is equal to two times T_{demag}, the output current Io is constant.

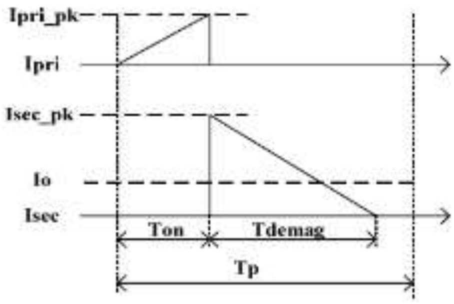


Figure 1. The current waveform of DCM mode

CV Operation Mode

In CV mode , AP8836 uses a pulse to sample V_{FB} and it is hold until the next sampling. The sampled voltage is compared with V_{REF_CV} and the error is amplified. The error amplified output reflects the load condition and controls the switching off time to regulate the output voltage, thus constant output voltage can be achieved. The relationship between the output voltage and V_{REF_CV} is

$$V_O=(V_{REF\_CV} \times \frac{R1+R2}{R2}) \times \frac{N_S}{N_{AUX}} \quad (3)$$

N_S means Secondary winding, N_{AUX} means Auxiliary winding

The AP8836 operates in PFM_QR mode during full load mode, since the peak current (Ipeak) of MOSFET is constant, the chip frequency decreases while the output current decreases. When the switching frequency approaches to 25kHz, the AP8836 enters PWM_QR mode, the chip frequency decreases slowly while the output current decreases, the Ipeak decreases while the output current decreases. Therefore the AP8836 can avoid audible noise, while achieving high efficiency at 25% load conditions. When Vcs decreases to 170mV, the AP8836 enters Standby mode. In this mode, Ipeak keeps around constant, the chip oscillator frequency decreases while the output current decreases. Figure 2 illustrates the relations of the switching frequency, Ipeak and Loading for AP8836.

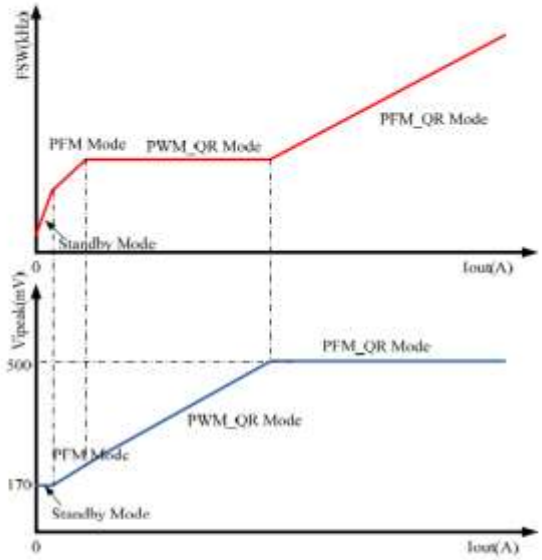


Figure 2. The Switching Frequency, Vipeak VS. LOAD

Current Sensing and Leading Edge Blanking

Cycle-by-Cycle current limiting is offered in AP8836. The switch current is detected by a sense resistor at CS pin. The CC set-point and maximum output power can be externally adjusted by external current sense resistor at CS pin. An internal leading edge blanking circuit chops off the sensed voltage spike at initial power MOSFET on-state so that the external RC filtering on sense input is no longer needed.

Programmable Cable Drop Compensation

In AP8836, an offset voltage is generated at FB pin by an internal current flowing into the divider resister, as shown in Figure 3. The Cable Drop Compensation block compensates the voltage drop across the cable. As the load current decreases from full load to no load, the voltage drop across the cable decreases. It can be programmed by adjusting the external resistor R2 or R1 at FB pin. The maximum compensation is

$$\frac{V_{cable}}{V_o} = \frac{I_{cable} \times (R1 // R2)}{2.5V} \quad (4)$$

Because of the influence of the chip's sampling position and devices of the system, the actual maximum compensation is less than theoretical value.

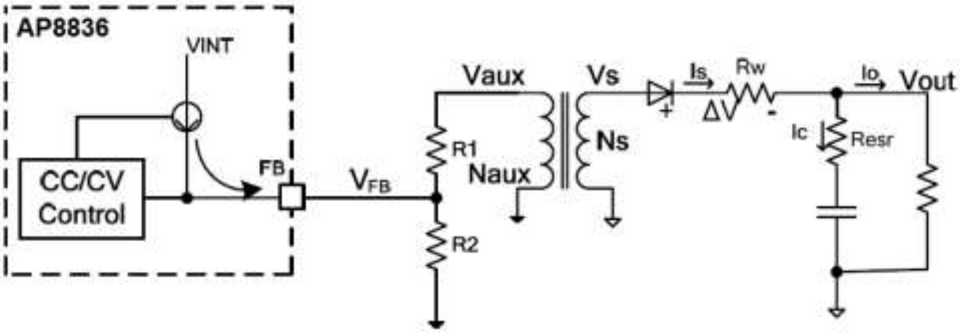


Figure 3. Icable

Reference Negative Temperature Compensation

As shown in Figure 3, the voltage of FB pin is

$$V_{FB} = K(V_o + \Delta V),\ K = \frac{R2 \times N_{AUX}}{(R1+R2) \times N_S} \quad (5)$$

Where ΔV has a negative temperature coefficient, K is a constant.

In AP8836, the voltage reference uses the negative temperature compensation technology. At room temperature, the voltage reference is 2.5V. The voltage reference (V_{REF_CV}) decreases while the temperature of chip increases. The reference negative temperature compensation block compensates the ΔV, thus the output voltage keeps constant at full range of temperature. The reference negative temperature compensation improves output precision.

Quasi-Resonant Switching

The AP8836 incorporates a unique proprietary quasi-resonant switching scheme that achieves valley-mode turn on for every switching cycle in CV mode. This unique feature greatly reduces the switching loss. The actual switching frequency can vary slightly cycle by cycle, providing the additional benefit of reducing EMI.

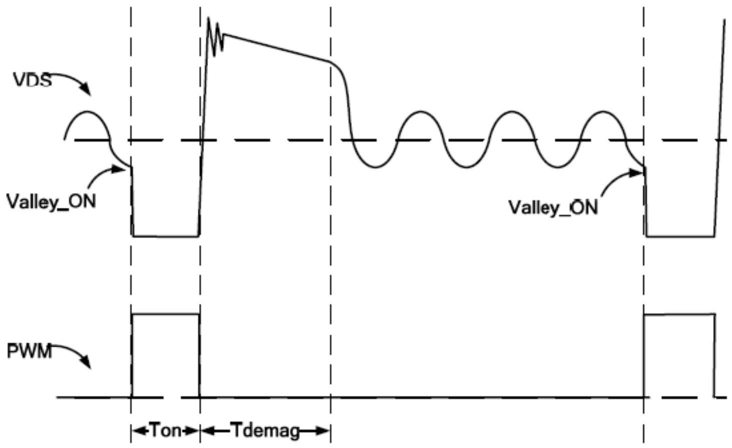
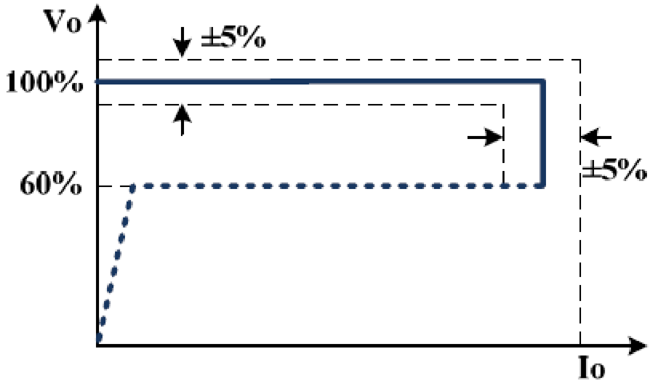


Figure 4. QR Mode

Protection Control

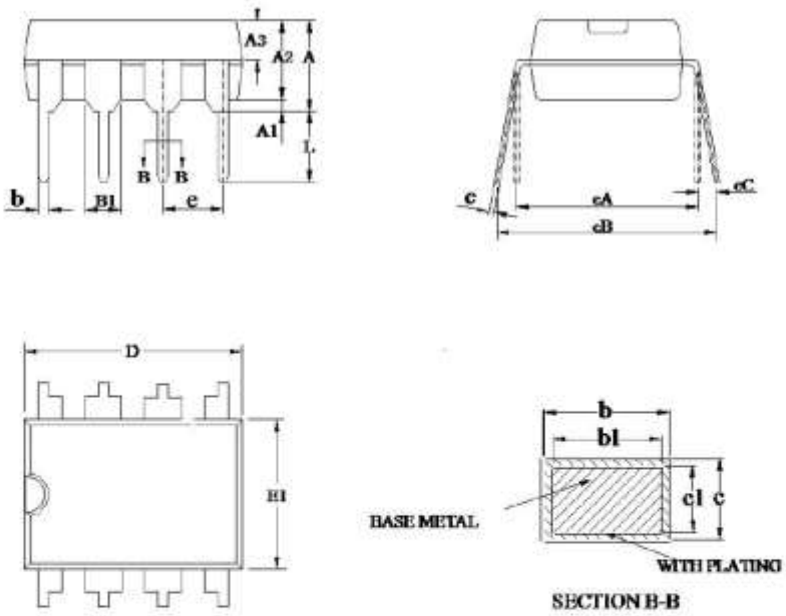
The AP8836 has several self-protection functions, such as Cycle-by-Cycle current limiting (OCP), Over-Voltage Protection, Over-Temperature Protection, Feedback Loop open Protection, Output short circuit Protection, CS resistor open/short circuit Protection and Under Voltage Lockout on V_{DD}. All protections are self-recovered.

OUTPUT FEATURES



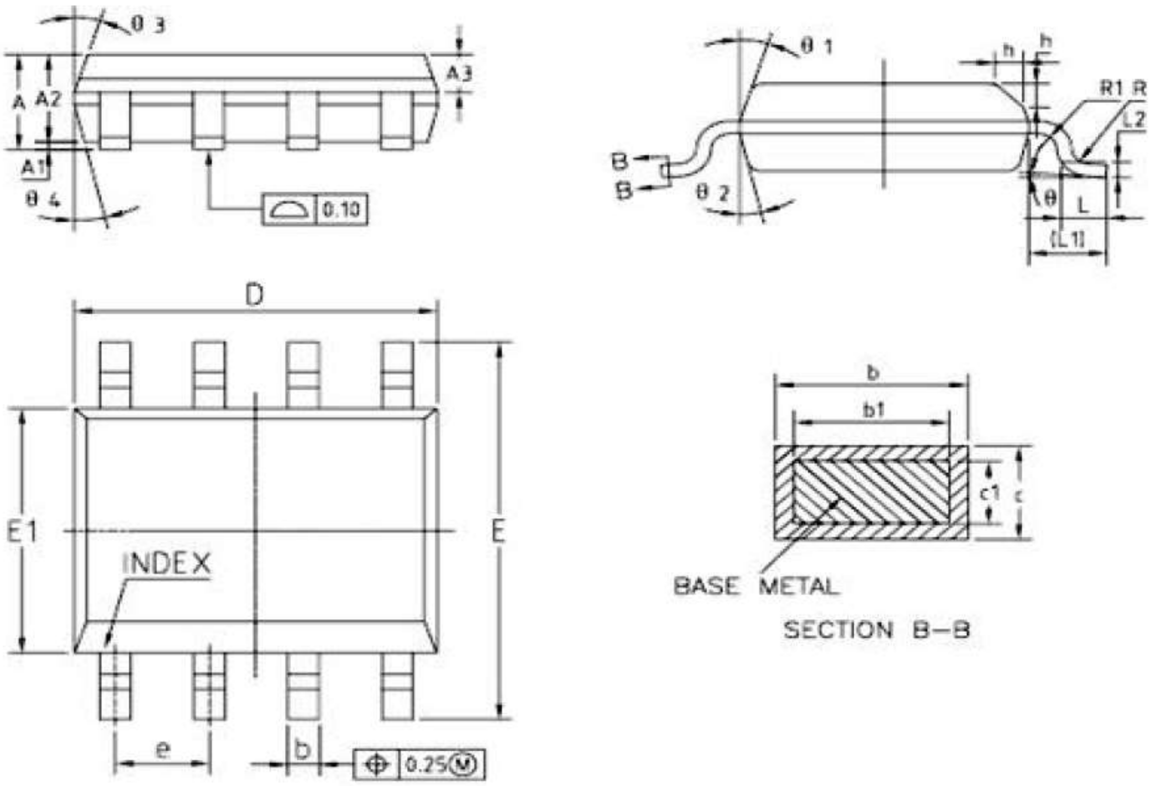
PACKAGE INFORMATION

Dimension in DIP8 (Unit: mm)



Symbol	Min	Max
A	3.60	4.00
A1	0.51	-
A2	3.00	3.40
A3	1.55	1.65
b	0.44	0.53
b1	0.43	0.48
B1	1.52BSC	
c	0.24	0.32
c1	0.23	0.27
D	9.05	9.45
E1	6.15	6.55
e	2.54BSC	
eA	7.62BSC	
eB	7.62	9.30
eC	0.00	0.84
L	3.00	-

Dimension in SOP8 (Unit: mm)



Symbol	Min	Max	Symbol	Min	Max
A	1.35	1.75	L	0.45	0.80
A1	0.10	0.25	L1	1.04REF	
A2	1.25	1.65	L2	0.25BSC	
A3	0.50	0.70	R	0.07	-
b	0.38	0.51	R1	0.07	-
b1	0.37	0.47	h	0.30	0.50
c	0.17	0.25	θ	0°	8°
c1	0.17	0.23	θ1	15°	19°
D	4.80	5.00	θ2	11°	15°
E	5.80	6.20	θ3	15°	19°
E1	3.80	4.00	θ4	11°	15°
e	1.27BSC				

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