

DESCRIPTION

The AO393 consist of two independent precision voltage comparators with a typical offset voltage of 1.0mV and high gain. They are specifically designed to operate from a single power supply over wide range of voltages.

Operation from split power supply is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.

The AO393 is available in SOP8 and DIP8 packages.

ORDERING INFORMATION

AiT provides all RoHS products

Package Type	Part Number		
SOP8	MO	AO393M8R	
SPQ: 4,000pcs/Reel	M8	AO393M8VR	
DIP8	P8	AO393P8U	
SPQ: 20pcs/Tube	P8	AO393M8VU	
	V: Halogen free Package R: Tape & Reel		
Note			
	U: Tube		

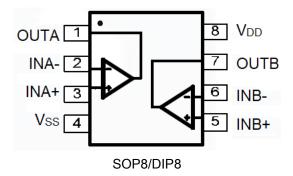
FEATURES

- Wide Supply Voltage Range
- Single Supply: 2.0V to 36V
- Dual Supplies: ±1.0V to ±18V
- Low Supply Current Drain: 0.6mA
- Low Input Bias Current: 25nA (Typical)
- Low Input Offset Current: ±5.0nA (Typical)
- Low Input Offset Voltage: 1.0mV (Typical)
- Input Common Mode Voltage Range Includes Ground
- Differential Input Voltage Range Equals to the Power Supply Voltage
- Low Output Saturation Voltage: 200mV at 4mA
- Open Collector Output
- Available in SOP8 and DIP8 packages

APPLICATION

- Vacuum Robot
- Single Phase UPS
- Server PSU
- Cordless Power Tool
- Wireless Infrastructure
- Building Automation
- Factory Automation & Control
- Motor Dives
- Infotainment & Cluster
- Battery Charger
- DC-DC Module
- PC Motherboard
- Communication Equipment

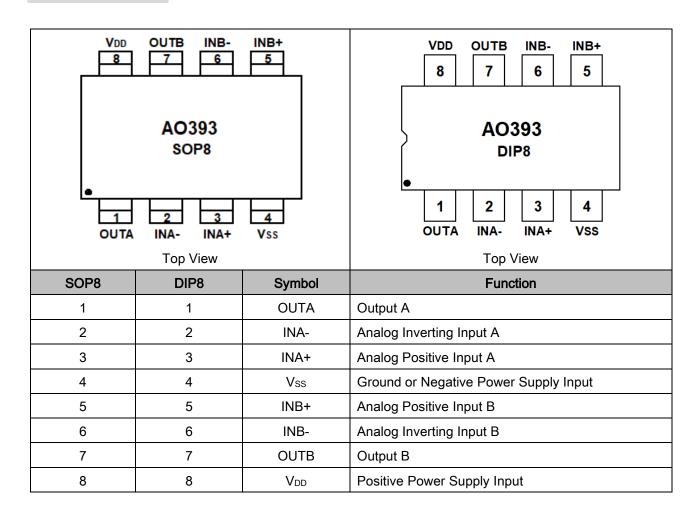
TYPICAL APPLICATION



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PIN DESCRIPTION



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ABSOLUTE MAXIMUM RATINGS

Vcc, Power Supply Voltage	±20V or 40V
V _{I(DIFF)} , Differential input voltage	40V
V _I , Input Voltage	-0.3V ~ 40V
T _{opr} , Operating Temperature Range	-25°C ~ 125°C
T _{STG} , Storage Temperature Range	-65°C ~ 150°C

Stress beyond above listed "Absolute Maximum Ratings" may lead permanent damage to the device. These are stress ratings only and operations of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

NOTE1: This input current will only exist when the voltage at any of the input leads is driven negative. It is due to the collector-base junction of the input PNP transistors becoming forward biased and thereby acting as input diode clamps. In addition to this diode action, there is also lateral NPN parasitic transistor action on the IC chip. This transistor action can cause the output voltages of the comparators to go to the V+ voltage level (or to ground for a large overdrive) for the time duration that an input is driven negative. This is not destructive and normal output states will re-establish when the input voltage, which was negative, again returns to a value greater than -0.3 V_{DC} at 25°C).

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Supply Voltage	V _{CC}		2	ı	36	V
Operating Temperature Range	TA		-40	1	85	°C

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ELECTRICAL CHARACTERISTICS

Limits in standard typeface are for T_A =25°C, bold typeface applies over T_A =-40°C to 85°CNOTE2, V_{CC} =5V, GND=0V, unless otherwise specified.

Parameter	Conditions		Min	Тур	Max	Unit
Less Coffee LV/ House	V ₀ =1.4V, R _S =0Ω, V _{CC} =5V to 30V		-	1.0	3.0	mV
Input Offset Voltage			-	-	5	
Lead Bird Order	I _{IN} + or I _{IN} - with ou	I _{IN} + or I _{IN} - with output in Linear		25	250	nA
Input Bias Current	Range, V _{CM} =0V	Range, V _{CM} =0V		-	400	
1			-	5.0	50	
Input Offset Current In+ - In-, V _{CM} =0V		-	-	200	nA	
Input Common-Mode Voltage Range ^{NOTE3}	V _{CC} = 30V		0	-	V _{CC} -1.5	٧
		\\ _ _ _ _ _ _ _ _ _ _ 	-	0.6	1.0	mA
Supply Current	V _{CC} = V _{CC} = V _{CC} =	$V_{CC} = 5V$	-	-	2	
			-	0.7	1.7	
		V _{CC} = 30V	-	-	3	
Voltage Gain	V _{CC} =15V, R _L ≥15kΩ, V _O =1V to 11V		50	200	-	V/mV
Large Signal Response Time	V _{IN} =TTL Logic Swing, V _{REF} =1.4V, V _{RL} =5V, R _L =5.1k		-	200	-	ns
Response Time	V _{RL} =5V, R _L =5.1k		-	1.3	-	μs
Output Sink Current	V _{IN} =1V, V _{IN} +=0, V _O =1.5V		6.0	16	-	mA
0.15.11.51.55.0	V _{IN} =0V, V _{IN} +=0, V _O =1.5V		-	0.1	-	nA
Output Leakage Current	V _{IN} =0V, V _{IN} +=0, V _O =30V		-	-	1	μΑ
0	V _{IN} =1V, V _{IN} +=0, I _{SINK} ≤4mA		-	200	400	mV
Saturation Voltage			-	-	500	
Thermal Resistance	DIP8		-	93	-	°C/A/
(Junction to Case)	SOP8		-	138	-	°C/W

NOTE2: These specifications are limited to -40°C \leq T_A \leq 85°C. Limits over temperature are guaranteed by design, but not tested in production.

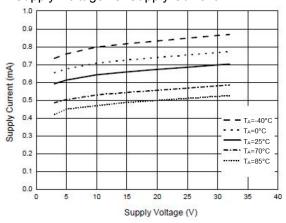
NOTE3: The input common-mode voltage of either input signal voltage should not be allowed to go negatively by more than 0.3V (at 25° C). The upper end of the common-mode voltage range is V_{CC} -1.5V (at 25° C), but either or both inputs can go to +36V without damages, independent of the magnitude of the V_{CC} .

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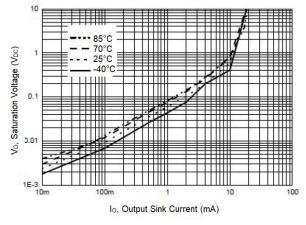


TYPICAL PERFORMANCE CHARACTERISTICS

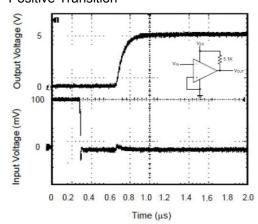
1. Supply Voltage vs. Supply Current



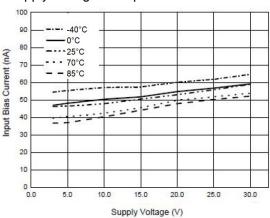
3. Output Sink Current vs. Saturation Voltage



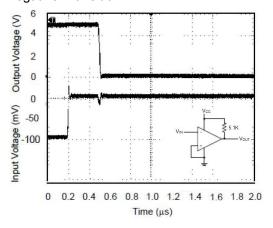
Response Time for 5mV Input Overdrive Positive Transition



2. Supply Voltage vs. Input Bias Current



 Response Time for 5mV Input Overdrive Negative Transition

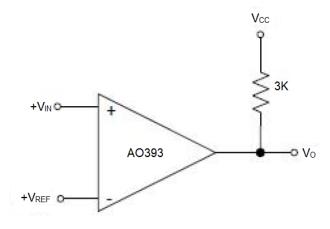


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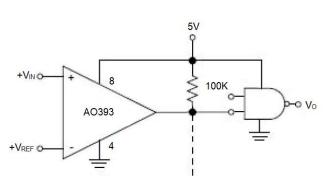


TYPICAL APPLICATIONS

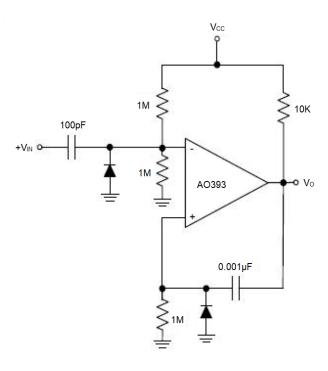
1. Basic Comparator



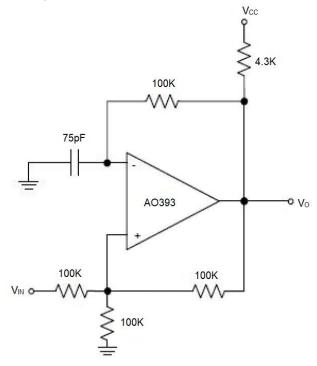
2. Driving CMOS



3. One Shot Multi-vibrator



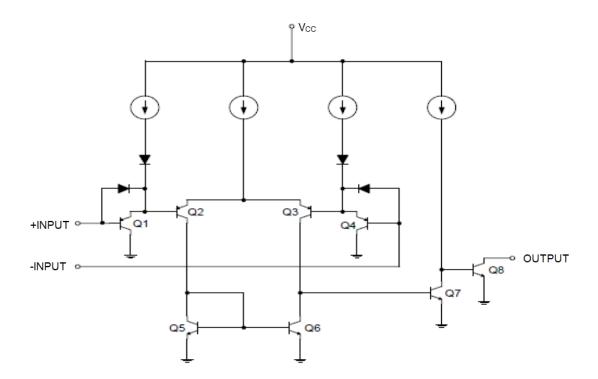
4. Square wave Oscillator



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BLOCK DIAGRAM

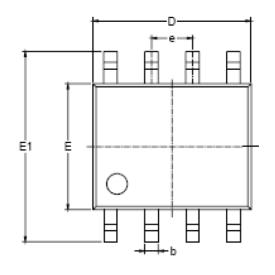


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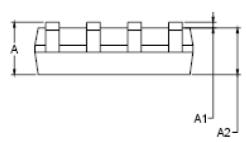


PACKAGE INFORMATION

Dimension in SOP8 (Unit: mm)





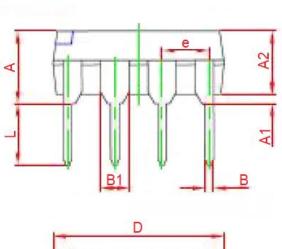


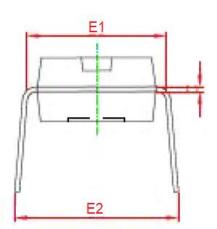
Symbol	Millim	neters	Inches		
	Min.	Max.	Min.	Max.	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
Е	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270	BSC	0.050	BSC	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	

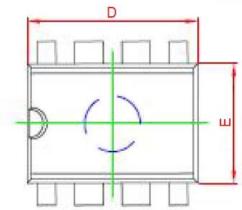
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Dimension in DIP8 (Unit: mm)







Cumphial	Millimeters		Inches			
Symbol	Min.	Max.	Min.	Max.		
Α	3.710	4.310	0.146	0.170		
A1	0.510	-	0.020	-		
A2	3.200	3.600	0.126	0.142		
В	0.380	0.570	0.015	0.022		
B1	1.524	BSC	0.060 BSC			
С	0.204	0.360	0.008	0.014		
D	9.000	9.400	0.354	0.370		
Е	6.200	6.600	0.244	0.260		
E1	7.320	7.920	0.288	0.312		
е	2.540 BSC		2.540 BSC		0.100	BSC
L	3.000	3.600	0.118	0.142		
E2	8.400	9.000	0.331	0.354		

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