



### DESCRIPTION

The AL1G11 is a single 3-input positive AND gate, designed for 1.65V to 5.5V Vcc operation.

The AL1G11 contains three AND gates and performs the Boolean function  $Y=A \cdot B \cdot C$  or  $Y=\overline{A + B + C}$  in positive logic.

The AL1G11 is fully specified for partial power down applications using IOFF. The IOFF circuitry disables the outputs, preventing damaging current backflow when the device is powered down.

The AL1G11 operates over an ambient temperature range of -40°C to +125°C.

The AL1G11 is available in SOT-26 and SC70-6 Packages.

### ORDERING INFORMATION

Package Type	Part Number	
SOT-26 SPQ: 3,000pcs/Reel	E6	AL1G11E6R
		AL1G11E6VR
SC70-6 SPQ:3,000pcs/Reel	C6	AL1G11C6R
		AL1G11C6VR
Note	V: Halogen free Package R: Tape & Reel	
AiT provides all RoHS products		

### FEATURES

- Operating Voltage Range: 1.65V to 5.5V
- Low Power Consumption: 1µA (Max).
- Operating Temperature Range: -40°C to +125°C
- Inputs Accept Voltage to 5.5V
- High Output Drive: ±24mA at Vcc=3.0V
- Available in SOT-26 and SC70-6 Packages

### APPLICATION

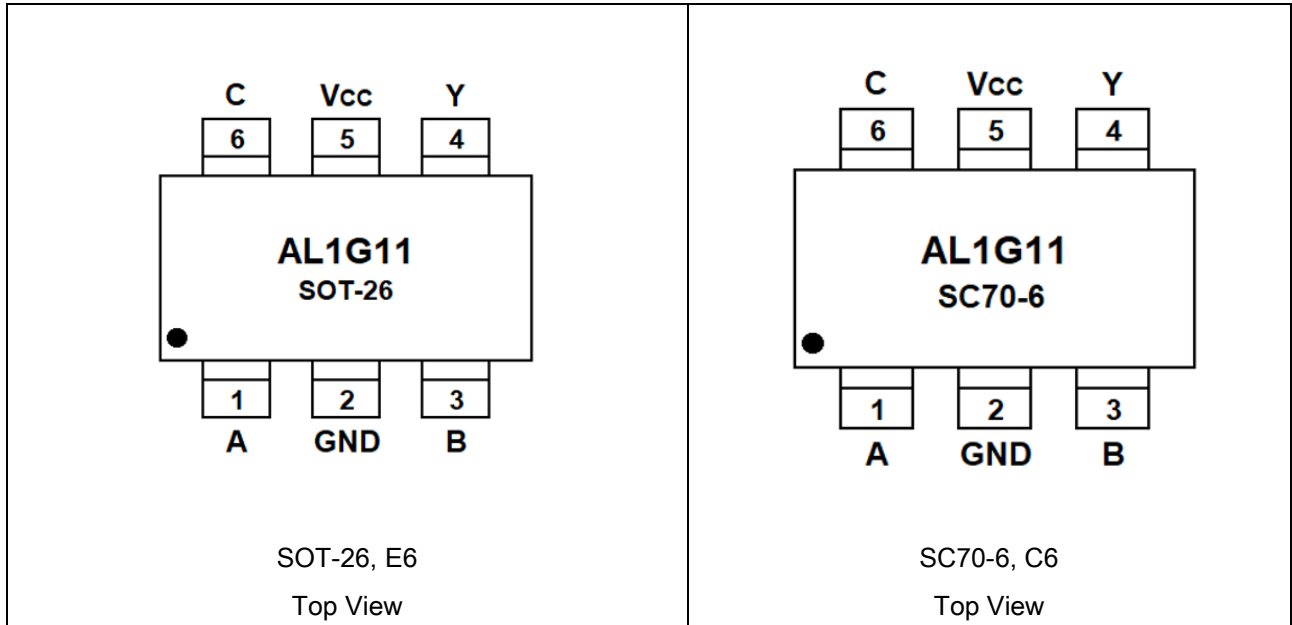
- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- PCs, Networking, Notebooks, PDAs
- Computer Peripherals, Hard Drives
- TV, DVD DVR Set Top Box
- Cell Phones, Personal Navigation / GPS
- MP3 players, Cameras, Video Recorders
- Active Noise Elimination
- Bar Code Scanner
- Blood Pressure Monitor
- CPAP Machine
- Fingerprint Identification
- Network Attached Storage (NAS)

### SIMPLIFIED SCHEMATIC





**PIN DESCRIPTION**



PIN#		Symbol	I/O	Function
SOT-26	SC70-6			
1	1	A	I	A Input
2	2	GND	P	Ground
3	3	B	I	B Input
4	4	Y	O	Output
5	5	V <sub>cc</sub>	P	Supply Power
6	6	C	I	C Input

I=input, O=output, P=Power



## ABSOLUTE MAXIMUM RATINGS

over operating free-air temperature range (unless otherwise noted) <sup>(1)</sup>

V <sub>CC</sub> , Supply Voltage Range		-0.5V ~ + 6.5V
V <sub>I</sub> , Input Voltage Range <sup>(1)</sup>		-0.5V ~ + 6.5V
V <sub>O</sub> , Voltage Range Applied to any Output in the High-Impedance or Power-Off State <sup>(1)</sup>		-0.5V ~ + 6.5V
V <sub>O</sub> , Voltage Range Applied to any Output in the High or Low State <sup>(1) (2)</sup>		-0.5V ~ V <sub>CC</sub> + 6.5V
I <sub>IK</sub> , Input Clamp Current	V <sub>I</sub> < 0	-50mA
I <sub>OK</sub> , Output Clamp Current	V <sub>O</sub> < 0	-50mA
I <sub>O</sub> , Continuous Output Current		±50mA
I <sub>O</sub> , Continuous Current through V <sub>CC</sub> or GND		±100mA
θ <sub>JA</sub> , Package Thermal Impedance <sup>(3)</sup>	SOT-26	230°C/W
	SC70-6	265°C/W
T <sub>J</sub> , Junction Temperature <sup>(4)</sup>		-65°C ~ +150°C
T <sub>STG</sub> , Storage Temperature		-65°C ~ +150°C

Stresses above may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the Electrical Characteristics are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

- (1) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.
- (2) The value of V<sub>CC</sub> is provided in the Recommended Operating Conditions table.
- (3) The package thermal impedance is calculated in accordance with JESD-51.
- (4) The maximum power dissipation is a function of T<sub>J</sub>(MAX), R<sub>θJA</sub>, and T<sub>A</sub>. The maximum allowable power dissipation at any ambient temperature is P<sub>D</sub> = (T<sub>J</sub>(MAX) - T<sub>A</sub>) / R<sub>θJA</sub>. All numbers apply for packages soldered directly onto a PCB.

## ESD RATINGS

Parameter	Symbol	Min	Unit
Human-Body Model (HBM), per ANSI/ESDA/JEDEC JS-001 <sup>(1)</sup>	V <sub>(ESD)</sub>	±4000	V
Charged-Device Model (CDM), per ANSI/ESDA/JEDEC JS-002 <sup>(2)</sup>	Electrostatic	±1500	
Machine Model (MM)	Discharge	±200	

- (1) JEDEC document JEP155 states that 500 V HBM allows safe manufacturing with a standard ESD control process.
- (2) JEDEC document JEP157 states that 250 V CDM allows safe manufacturing with a standard ESD control process.



**RECOMMENDED OPERATING CONFITIONS**

TA=25°C, unless otherwise noted.\*

Parameter	Symbol	Conditions	Min	Typ.	Max	Unit
Supply Voltage	V <sub>CC</sub>	Operating	1.65	-	5.5	V
		Data Retention only	1.50	-	-	
High-Level Input Voltage	V <sub>IH</sub>	V <sub>CC</sub> = 1.65 V ~1.95 V	0.65x V <sub>CC</sub>	-		V
		V <sub>CC</sub> = 2.3V ~2.7 V	1.70	-		
		V <sub>CC</sub> = 3 V ~3.6 V	2	-		
		V <sub>CC</sub> = 4.5 V ~5.5 V	0.70x V <sub>CC</sub>	-		
Low-Level Input Voltage	V <sub>IL</sub>	V <sub>CC</sub> = 1.65 V ~1.95 V	-	-	0.35x V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3V ~2.7 V	-	-	0.70	
		V <sub>CC</sub> = 3 V ~3.6 V	-	-	0.80	
		V <sub>CC</sub> = 4.5 V ~5.5 V	-	-	0.30x V <sub>CC</sub>	
Input Voltage	V <sub>I</sub>	-	0	-	5.50	V
Output Voltage	V <sub>O</sub>	-	0	-	V <sub>CC</sub>	V
Input Transition Rise or Fall	Δt/Δv	V <sub>CC</sub> = 1.8V±0.15V, 2.5V±0.2V	-	-	20	ns/V
		V <sub>CC</sub> = 3.30 V ± 0.3 V	-	-	10	
		V <sub>CC</sub> = 5 V ± 0.5V	-	-	5	
Operating Temperature	T <sub>A</sub>	-	-40	-	+125	°C

\* All unused inputs of the device must be held at VCC or GND to ensure proper device operation.



**DC CHARACTERISTICS**

Parameter		Conditions	Min	Typ.	Max	Unit
V <sub>OH</sub>		I <sub>OH</sub> = -100 μA, V <sub>CC</sub> =1.65~5.5V	V <sub>CC</sub> -0.1	-	-	V
		I <sub>OH</sub> = -4mA, V <sub>CC</sub> =1.65V	1.20	-	-	
		I <sub>OH</sub> = -8mA, V <sub>CC</sub> =2.3V	1.9	-	-	
		I <sub>OH</sub> = -16mA, V <sub>CC</sub> =3V	2.4	-	-	
		I <sub>OH</sub> = -24mA, V <sub>CC</sub> =3V	2.3	-	-	
		I <sub>OH</sub> = -32mA, V <sub>CC</sub> =4.5V	3.8	-	-	
V <sub>OL</sub>		I <sub>OL</sub> = 100 μA, V <sub>CC</sub> =1.65~5.5V	-	-	0.10	V
		I <sub>OL</sub> = 4mA, V <sub>CC</sub> =1.65V	-	-	0.45	
		I <sub>OL</sub> = 8mA, V <sub>CC</sub> =2.3V	-	-	0.30	
		I <sub>OL</sub> = 16mA, V <sub>CC</sub> =3V	-	-	0.40	
		I <sub>OL</sub> = 24mA, V <sub>CC</sub> =3V	-	-	0.55	
		I <sub>OL</sub> = 32mA, V <sub>CC</sub> =4.5V	-	-	0.55	
I <sub>i</sub>	All Inputs	V <sub>i</sub> = 5.5V or GND, V <sub>CC</sub> =0V~5.5V	+25°C	±0.1	±1	μA
			-40°C ~ +125°C	-	±5	
I <sub>off</sub>		V <sub>i</sub> or V <sub>o</sub> = 5.5V, V <sub>CC</sub> =0V	+25°C	±0.1	±1	μA
			-40°C ~ +125°C	-	±10	
I <sub>CC</sub>		V <sub>i</sub> = 5.5V or GND, I <sub>o</sub> =0, V <sub>CC</sub> =1.65V~5.5V	+25°C	0.1	1	μA
			-40°C ~ +125°C	-	10	
ΔI <sub>CC</sub>		One input at V <sub>CC</sub> -0.6V, Other inputs at V <sub>CC</sub> or GND, V <sub>CC</sub> =3V~5.5V	-40°C ~ +125°C	-	500	μA
C <sub>i</sub> (Input Capacitance)		V <sub>i</sub> = V <sub>CC</sub> or GND, V <sub>CC</sub> =3.3V	+25°C	-	4	pF

All unused inputs of the device must be held at VCC or GND to ensure proper device operation

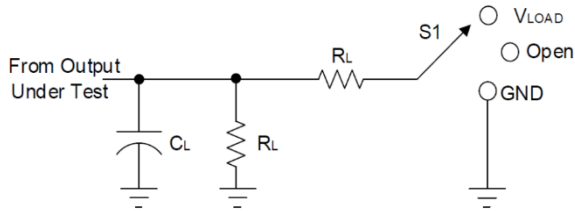
**AC CHARACTERISTICS**

Parameter	Symbol	Conditions		Min	Typ.	Max	Unit
Propagation Delay	t <sub>pd</sub>	V <sub>CC</sub> =1.8V±0.15V	C <sub>L</sub> =30pF, R <sub>L</sub> =1KΩ	-	13.60	-	ns
		V <sub>CC</sub> =2.5V±0.2V	C <sub>L</sub> =30pF, R <sub>L</sub> =500Ω	-	5.50	-	
		V <sub>CC</sub> =3.3V±0.3V	C <sub>L</sub> =50pF, R <sub>L</sub> =500Ω	-	4.20	-	
		V <sub>CC</sub> =5V±0.5V	C <sub>L</sub> =50pF, R <sub>L</sub> =500Ω	-	3.70	-	
Power Dissipation Capacitance	C <sub>pd</sub>	V <sub>CC</sub> =1.8V	f=10MHz	-	16	-	pF
		V <sub>CC</sub> =2.5V		-	18	-	
		V <sub>CC</sub> =3.3V		-	18	-	
		V <sub>CC</sub> =5V		-	20	-	

All unused inputs of the device must be held at VCC or GND to ensure proper device operation.

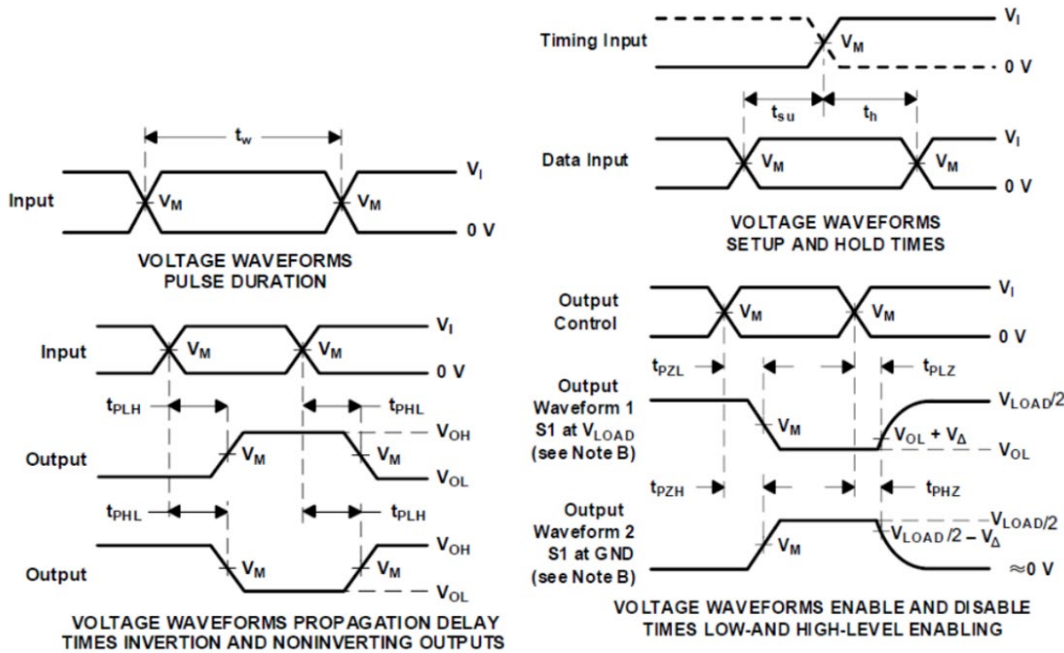


**PARAMETER MEASUREMENT INFORMATION**



TEST	S1
$t_{PLH}/t_{PHL}$	Open
$t_{PIZ}/t_{PZL}$	$V_{LOAD}$
$t_{PHZ}/t_{PZH}$	GND

$V_{CC}$	Inputs		$V_M$	$V_{LOAD}$	$C_L$	$R_L$	$V_{\Delta}$
	$V_I$	$t_r/t_f$					
$1.8V \pm 0.15V$	$V_{CC}$	$\leq 2ns$	$V_{CC}/2$	$2 \times V_{CC}$	30pF	1k $\Omega$	0.15V
$2.5V \pm 0.2V$	$V_{CC}$	$\leq 2ns$	$V_{CC}/2$	$2 \times V_{CC}$	30pF	500 $\Omega$	0.15V
$3.3V \pm 0.3V$	3V	$\leq 2.5ns$	1.5V	6V	50pF	500 $\Omega$	0.3V
$5V \pm 0.5V$	$V_{CC}$	$\leq 2.5ns$	$V_{CC}/2$	$2 \times V_{CC}$	50pF	500 $\Omega$	0.3V



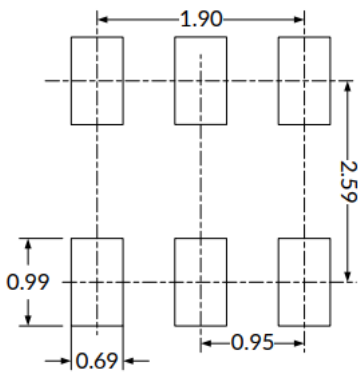
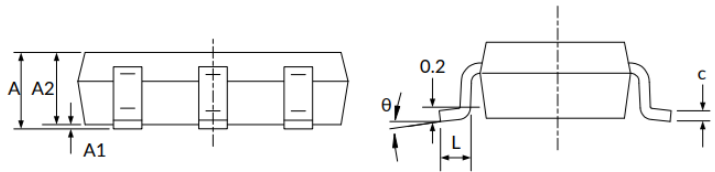
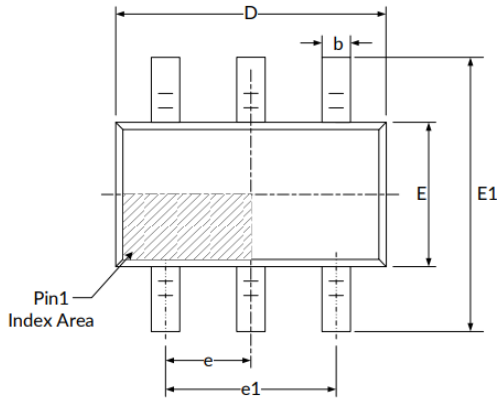
**Fig 1. Load Circuit and Voltage Waveforms**

- (A)  $C_L$  includes probe and jig capacitance.
- (B) Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.
- (C) All input pulses are supplied by generators having the following characteristics:  $PRR \leq 10 \text{ MHz}$ ,  $Z_o = 50 \Omega$ .
- (D) The outputs are measured one at a time, with one transition per measurement.
- (E)  $t_{PLZ}$  and  $t_{PHZ}$  are the same as  $t_{dis}$ .
- (F)  $t_{LPZ}$  and  $t_{PZH}$  are the same as  $t_{en}$ .
- (G)  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{pd}$
- (H) All parameters and waveforms are not applicable to all devices.



**PACKAGE INFORMATION**

Dimension in SOT-26 (Unit: mm)

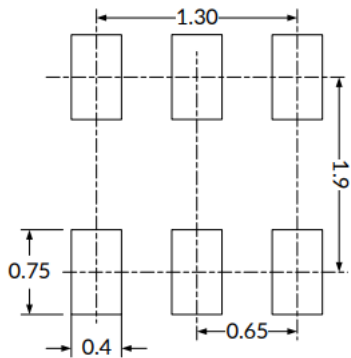
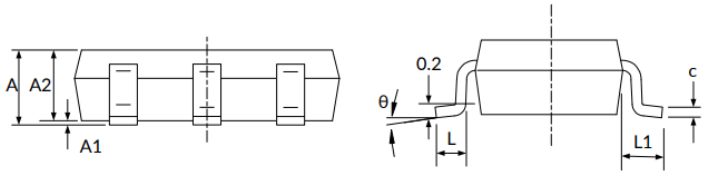
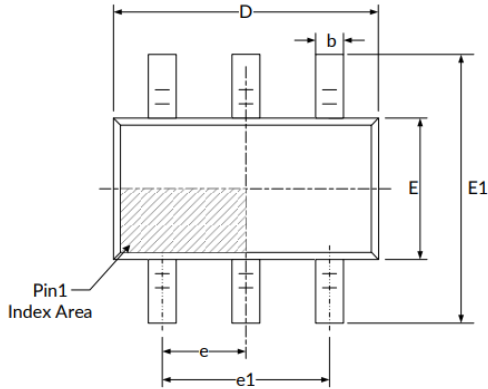


**Recommended Land Pattern (Unit: mm)**

Symbol	Millimeters	
	Min	Max
A	1.050	1.250
A1	0.000	0.100
A2	1.050	1.150
b	0.300	0.500
c	0.100	0.200
D	2.820	3.020
e	0.950 BSC	
e1	1.800	2.000
E	1.500	1.700
E1	2.650	2.950
L	0.300	0.600
θ	0°	8°



Dimension in SC70-6(Unit: mm)



Recommended Land Pattern (Unit: mm)

Symbol	Millimeters	
	Min	Max
A	0.900	1.100
A1	0.000	0.100
A2	0.900	1.000
b	0.150	0.350
c	0.080	0.150
D	2.000	2.200
E	1.150	1.350
E1	2.150	2.450
e	0.650 BSC	
e1	1.300 BSC	
L	0.260	0.460
H	0.525 TYP	
$\theta$	0°	8°





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