



DESCRIPTION

The AM50N06 is available in PDFN8(5x6) Package.

VDSS	RDSON	ID
	V _{GS} =10V TYP.	
60V	12mΩ	50A

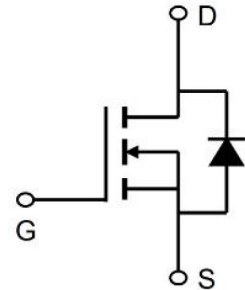
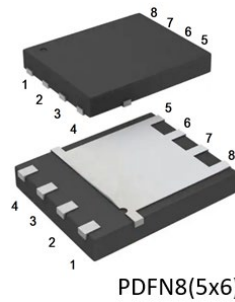
FEATURE

- Fast Switching
- Low on Resistance
- Low Gate Charge
- 100% Single Pulse Avalanche Energy Test
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APPLICATION

- Load Switch
- PWM Application
- Power management

PIN DESCRIPTION



MECHANICAL DATA

- Case: Molded plastic
- Mounting Position: Any
- Molded Plastic: UL Flammability Classification Rating 94V-0
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Solder bath temperature 275°C maximum,10s per JESD 22-B106

Pin#	Symbol	Function
1,2,3	S	Source
4	G	Gate
5,6,7,8	D	Drain

ORDERING INFORMATION

Package Type	Part Number	
PDFN8(5x6) SPQ: 5,000pcs/Reel	PJ8	AM50N06PJ8R
		AM50N06PJ8VR
Note	R: Tape & Reel V: Halogen free Package	
AiT provides all RoHS products		



ABSOLUTE MAXIMUM RATINGS

T_C=25°C, unless otherwise Noted

V _{DS} , Drain-Source Voltage	60V
V _{GS} , Gate-Source Voltage	±20V
I _D , Continue Drain Current	50A
I _{DM} *, Pulsed Drain Current	200A
E _{AS} , Single Pulsed Avalanche Energy	80mJ
P _D , Power Dissipation	75W
R _{θJC} , Thermal Resistance, Junction to Case	2°C/W
R _{θJA} , Thermal Resistance, Junction to Ambient	62°C/W
T _J , Operating Temperature Range	+150°C
T _{STG} , Storage Temperature Range	-55°C~+150°C

Stresses above may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the Electrical Characteristics are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

*Pulse test 300us pulse width, 2% duty cycle.



ELECTRICAL CHARACTERISTICS

T_C = 25°C, unless otherwise Noted

Parameter	Symbol	Conditions	Min	Typ.	Max	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	60	-	-	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} = 60V, V _{GS} =0V	-	-	1	μA
Gate Leakage Current	I _{GSS}	V _{DS} = 0V, V _{GS} =±20V	-	-	±100	nA
Drain-Source On- State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =30A	-	12	17	mΩ
		V _{GS} =4.5V, I _D =20A	-	16	25	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	1	-	2.50	V
Input Capacitance	C _{iss}	V _{DS} = 25V, V _{GS} =0V, f=1MHz	-	2030	-	pF
Output Capacitance	C _{oss}		-	130	-	
Reverse Transfer Capacitance	C _{rss}		-	115	-	
Total Gate Charge	Q _g	V _{DS} = 30V, I _D =30A, V _{GS} =10V	-	45	-	nC
Gate-Source charge	Q _{gS}		-	8	-	
Gate-Drain charge	Q _{gd}		-	11	-	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 30V, I _D =30A, V _{GS} = 10V, R _G =1.8Ω	-	11	-	ns
Rise Time	t _r		-	79	-	
Turn-Off Delay Time	t _{d(off)}		-	33	-	
Fall Time	t _f		-	105	-	
Maximum Body-Diode Continuous Current	I _S	-	-	-	50	A
Maximum Body-Diode Pulsed Current	I _{SM}	-	-	-	200	
Drain-Source Diode Forward Voltage	V _{SD}	I _{SD} =30A	-	-	1.20	V
Reverse Recovery Time	t _{rr}	I _F =I _S , I _{SD} =30A,	-	14	-	ns
Reverse Recovery Charge	Q _{rr}	V _{GS} =0V, di/dt=100A/us	-	10	-	nC



TYPICAL PERFORMANCE CHARACTERISTICS

Fig 1. Output Characteristics

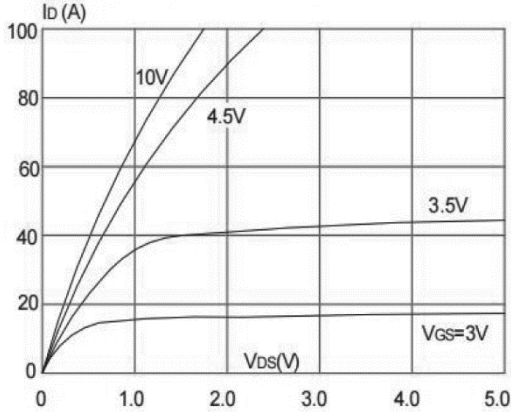


Fig 2. Typical Transfer Characteristics

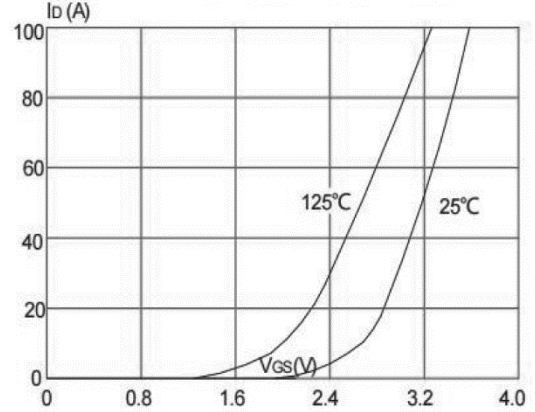


Fig 3. On-Resistance vs. Drain Current

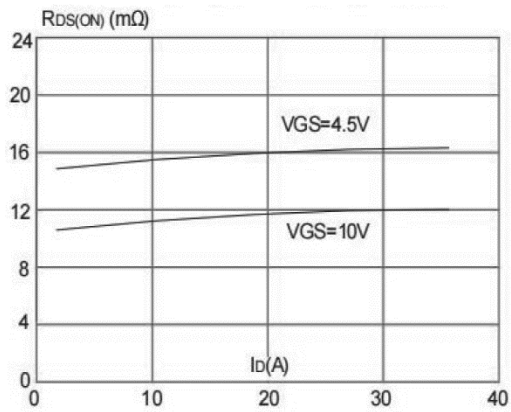


Fig 4. Body Diode Characteristics

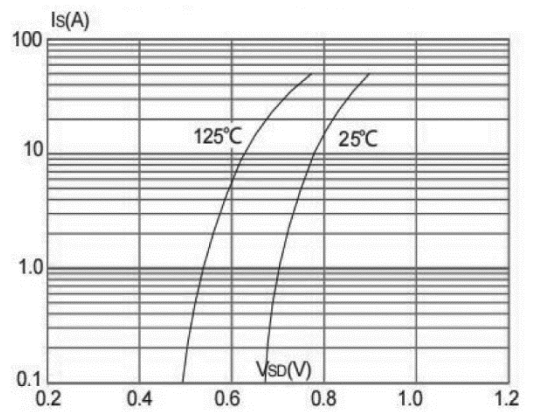


Fig 5. Gate Charge Characteristics

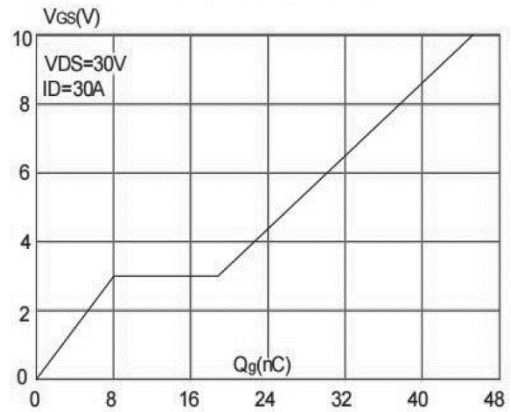


Fig 6. Capacitance Characteristics

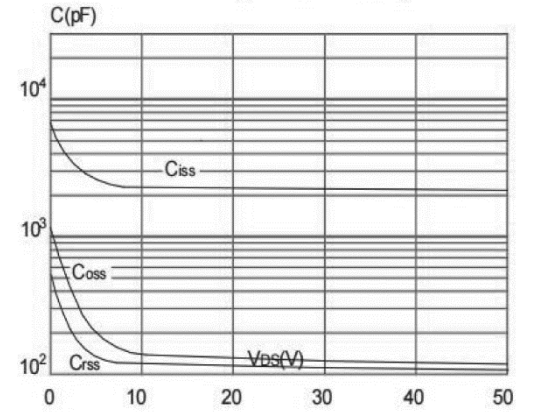




Fig 7. Normalized Breakdown Voltage vs. Junction Temperature

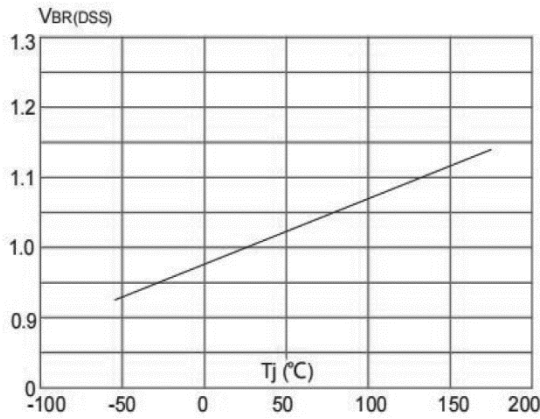


Fig 8. Normalized On Resistance vs. Junction Temperature

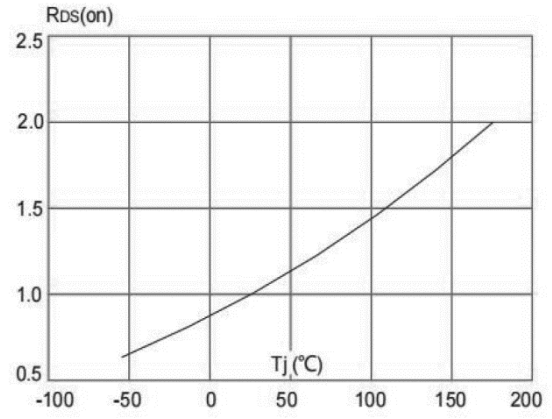


Fig 9. Maximum Safe Operating Area

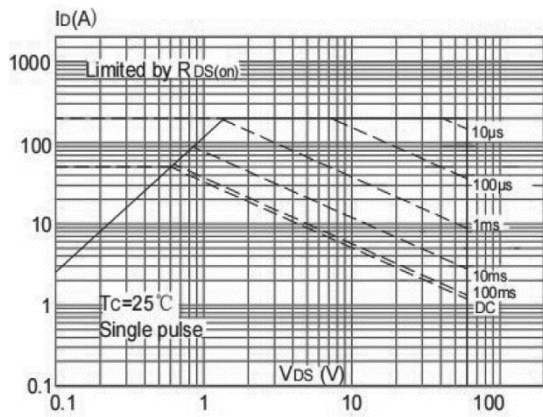


Fig 10. Maximum Continuous Drain Current vs. Case Temperature

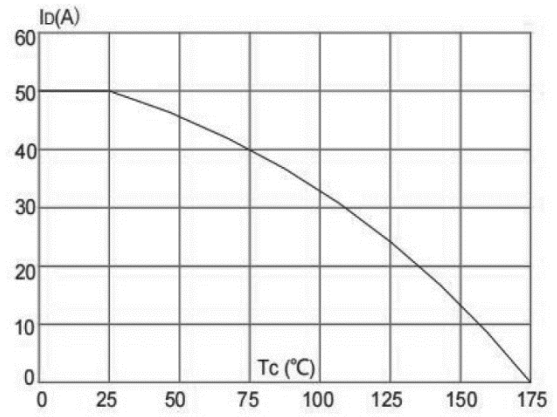
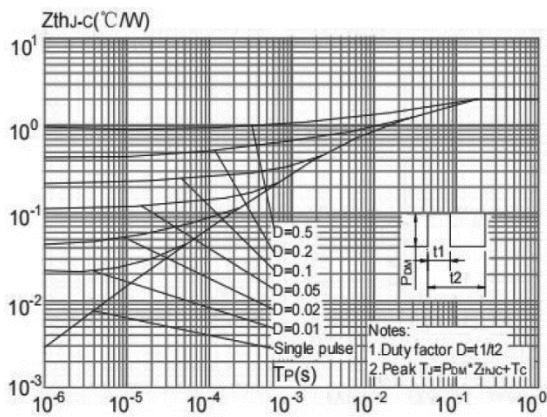


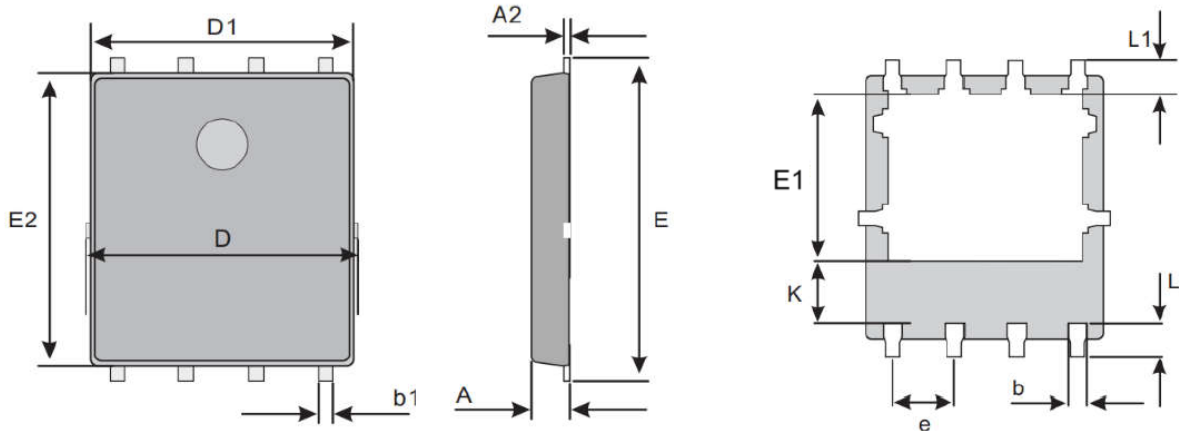
Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case





PACKAGE INFORMATION

Dimension in PDFN8 (5x6) (Unit: mm)



Symbol	Millimeter	
	Min.	Max.
A	0.900	1.200
A2	0.204	0.304
b	0.400 REF.	
b1	0.200	0.400
D	5.000	5.300
D1	4.840	5.240
E	5.700	5.900
E1	3.275	3.675
E2	5.690	6.090
e	1.270 BSC.	
K	1.290 REF.	
L	0.585	0.785
L1	0.700 TYP.	



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