

**DESCRIPTION**

The AM50N06T is available in the TO-220 Package.

VDS	RDS(ON)	ID
60V	11.3mΩ	50A

APPLICATIONS

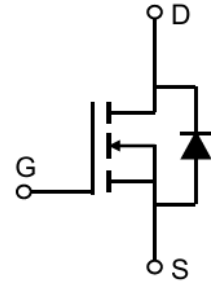
- Load Switch
- PWM Application
- Power Management

ORDERING INFORMATION

Package Type	Part Number	
TO-220 SPQ: 50pcs /Tube	T3	AM50N06T3VU
Note	V: Halogen free Package U: Tube Package	
AiT provides all RoHS products		

FEATURES

- 60V, 50A
 $R_{DS(ON)}$ Typ. = 11.3mΩ @ $V_{GS} = 10V$
 $R_{DS(ON)}$ Typ. = 13.7mΩ @ $V_{GS} = 4.5V$
- Advanced Split Gate Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge

PIN DESCRIPTION

Pin#	Symbol	Function
1	G	Gate
2	D	Drain
3	S	Source

ABSOLUTE MAXIMUM RATINGS

$T_J = 25^\circ\text{C}$, unless otherwise Noted

V_{DS} , Drain-to-Source Voltage		60V
V_{GS} , Gate-to-Source Voltage		$\pm 20V$
I_D , Continue Drain Current	$T_C = 25^\circ\text{C}$	50A
	$T_C = 100^\circ\text{C}$	30A
I_{DM} , Pulsed Drain Current ⁽¹⁾		200A
E_{AS} , Single Pulse Avalanche Energy ⁽²⁾		72mJ
P_D , Power Dissipation	$T_C = 25^\circ\text{C}$	75W
$R_{\theta JC}$, Thermal Resistance, Junction to Case		1.67°C/W
T_J , Operating Junction Temperature Range		-55°C~+150°C
T_{STG} , Storage Temperature Range		-55°C~+150°C

Stresses above may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the Electrical Characteristics are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

(1) Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

(2) EAS condition: Starting $T_J = 25^\circ\text{C}$, $V_{DD} = 50V$, $V_G = 10V$, $R_G = 25\Omega$, $L = 0.5mH$, $I_{AS} = 47A$

**ELECTRICAL CHARACTERISTICS**T_J = 25°C, unless otherwise Noted

Parameter	Symbol	Conditions	Min	Typ.	Max	Unit
Off Characteristic						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 60V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{DS} = 0V, V _{GS} =±20V	-	-	±100	nA
On Characteristic						
Static Drain-Source ON-Resistance *	R _{DS(ON)}	V _{GS} =10V, I _D =30A	-	11.3	14.7	mΩ
		V _{GS} =4.5V, I _D =20A	-	13.7	18	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1	1.5	2	V
Dynamic Characteristic						
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1MHz	-	1967	-	pF
Output Capacitance	C _{oss}		-	136	-	
Reverse Transfer Capacitance	C _{rss}		-	117	-	
Total Gate Charge	Q _G	V _{DS} = 30V, I _D =30A, V _{GS} = 0V ~ 10V	-	45	-	nC
Gate-Source charge	Q _{gS}		-	8	-	
Gate-Drain charge	Q _{gd}		-	11	-	
Switching Characteristic						
Turn-On Delay Time	t _{d(on)}	V _{DD} = 30V, R _{GEN} =1.8Ω V _{GS} = 10V, I _D =30A	-	11	-	ns
Rise Time	t _r		-	79	-	
Turn-Off Delay Time	t _{d(off)}		-	33	-	
Fall Time	t _f		-	107	-	
Drain-Source Diode Characteristics and Max Ratings						
Maximum Continuous Drain to Source Diode Forward Current	I _S	-	-	-	50	A
Maximum Pulsed Drain to Source Diode Forward Current	I _{SM}	-	-	-	200	A
Drain to Source Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =30A	-	-	1.20	V
Body Diode Reverse Recovery Time	t _{rr}	I _F =30A, di/dt=100A/μs	-	14	-	ns
Body Diode Reverse Recovery Charge	Q _{rr}		-	10	-	nC

* Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.



TYPICAL PERFORMANCE CHARACTERISTICS

Fig 1. Output Characteristics

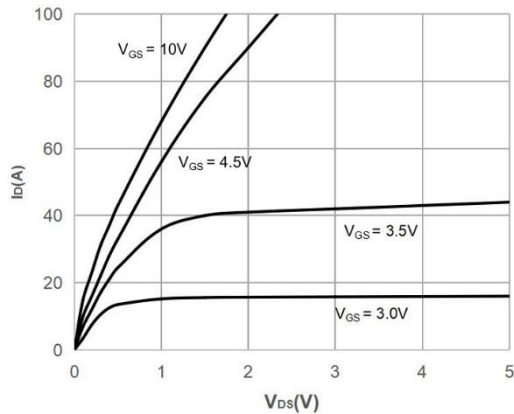


Fig 2. Typical Transfer Characteristics

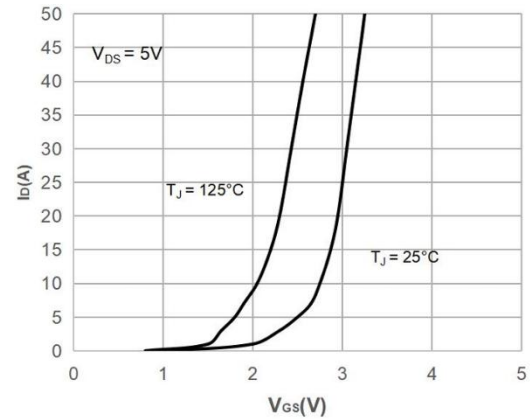


Fig 3. On-Resistance vs. Drain Current

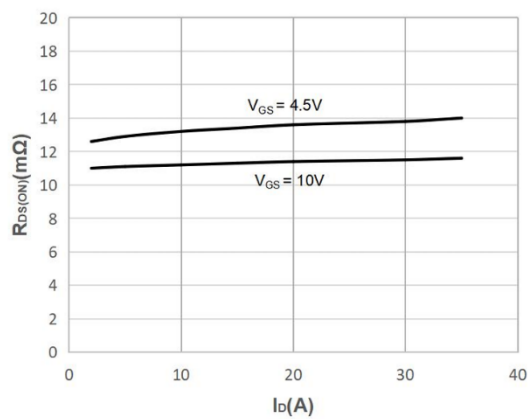


Fig 4. Body Diode Characteristics

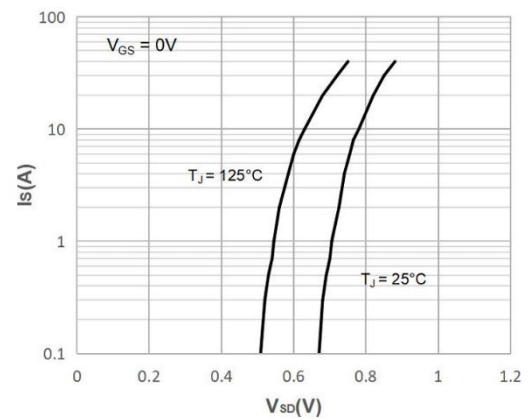


Fig 5. Gate Charge Characteristics

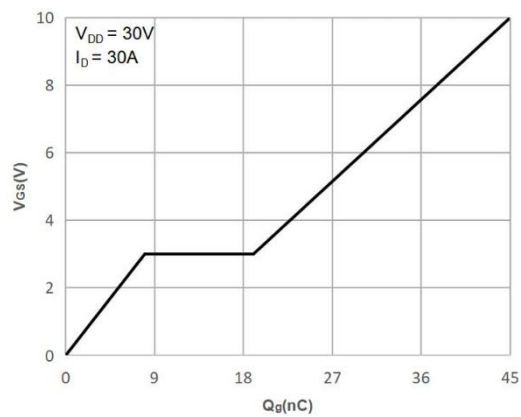


Fig 6. Capacitance Characteristics

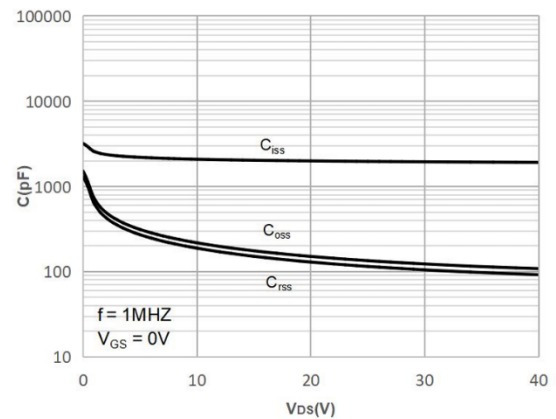




Fig 7. Normalized Breakdown Voltage vs. Junction Temperature

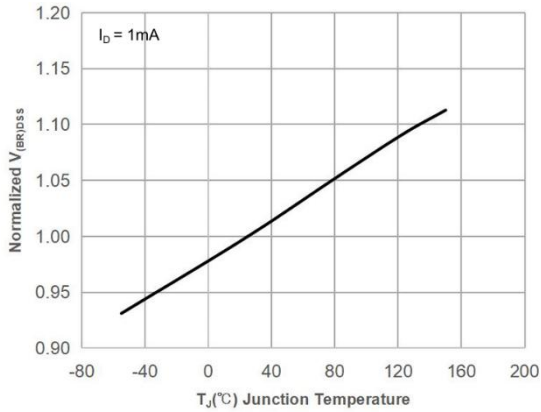


Fig 8. Normalized On-Resistance vs. Junction Temperature

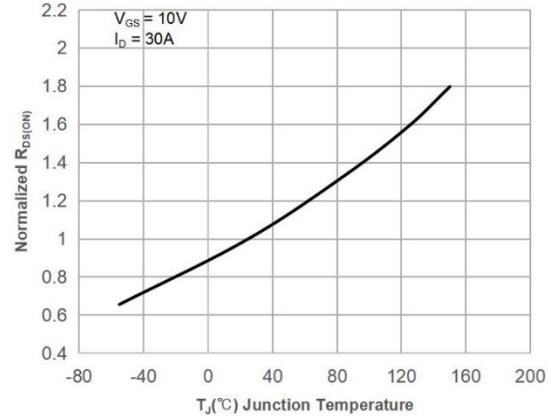


Fig 9. Maximum Safe Operating Area

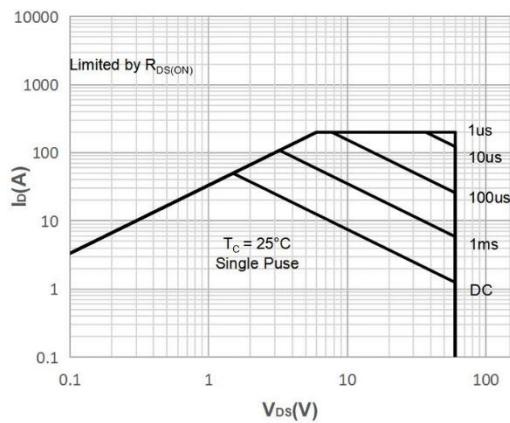


Fig 10. Maximum Continuous Drain Current vs. Case Temperature

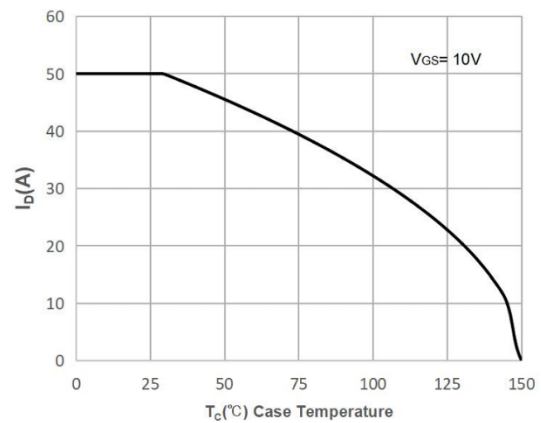


Fig 11. Normalized Maximum Transient Thermal Impedance

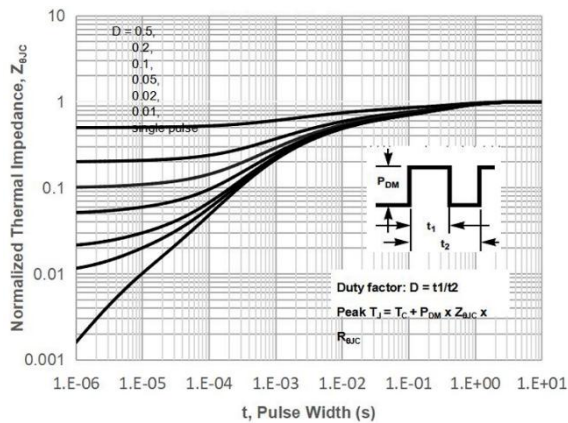


Fig 12. Peak Current Capacity

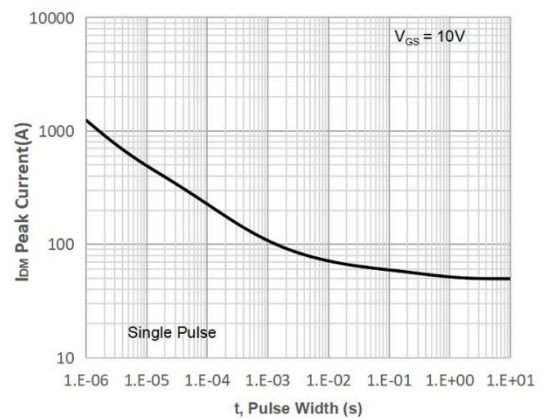




Fig 13. Gate Charge Test Circuit & Waveform

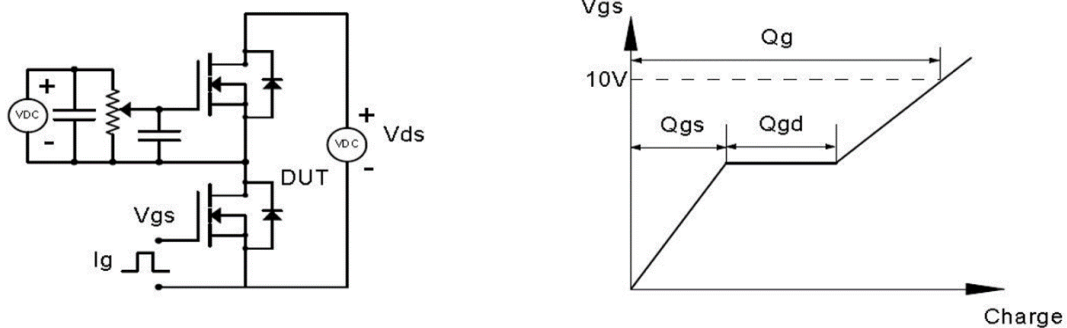


Fig 14. Resistive Switching Test Circuit & Waveforms

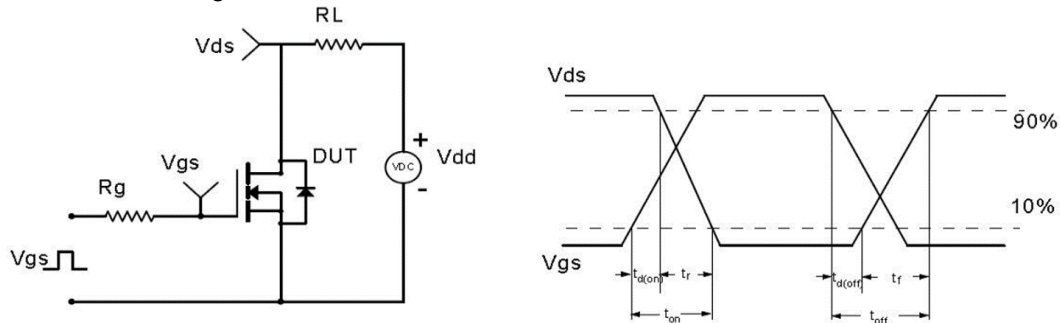


Fig 15. Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

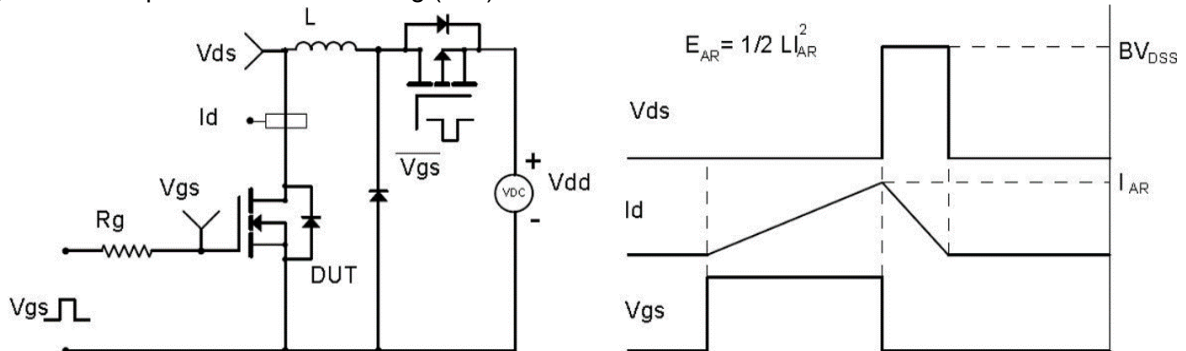
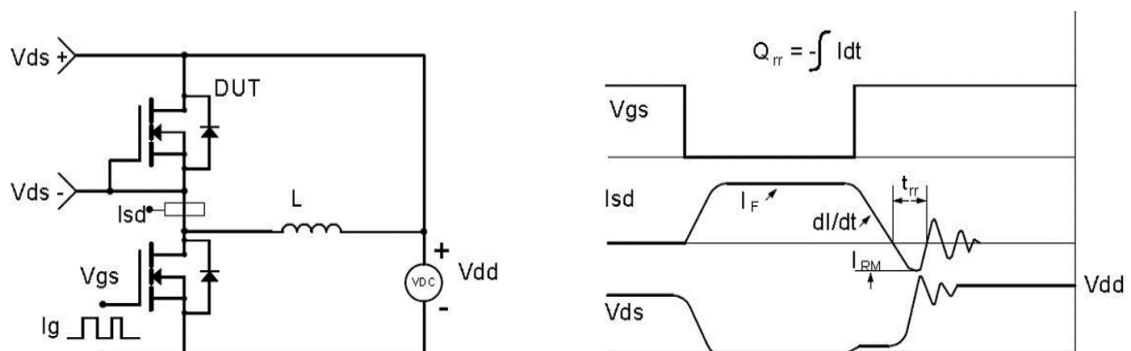


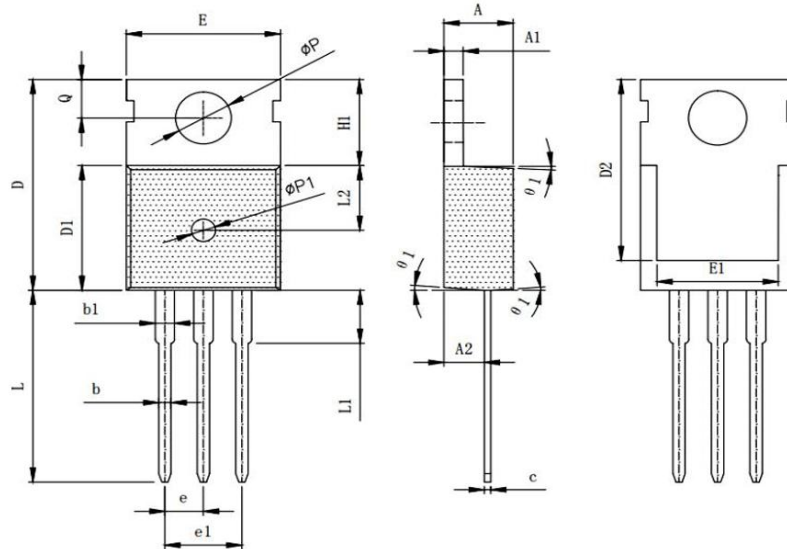
Fig 16. Diode Recovery Test Circuit & Waveforms





PACKAGE INFORMATION

Dimension in TO-220(Unit: mm)



Symbol	Millimeter	
	Min.	Max.
A	4.400	4.600
A1	1.250	1.350
A2	2.300	2.500
b	0.700	0.900
b1	1.250	1.450
c	0.400	0.600
D	15.500	16.100
D1	9.100	9.300
D2	12.730	12.930
e	2.540 BSC.	
e1	5.080 BSC.	
E	9.700	10.200
E1	7.600	8.400
H1	6.300	6.800
L	12.750	13.500
L1	-	3.100
L2	4.300	4.900
Q	2.700	2.900
ϕP	3.500	3.700
$\phi P1$	1.400	1.600
$\theta 1$	2°	6°



IMPORTANT NOTICE

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