



DESCRIPTION

The AM80R330TF is available in the TO-220F Package.

BV_{DSS}	R_{DS(ON)}	I_D
800V	0.33Ω	13A

APPLICATIONS

- Power factor correction (PFC).
- Switched mode power supplies (SMPS).
- Uninterruptible power supply (UPS)

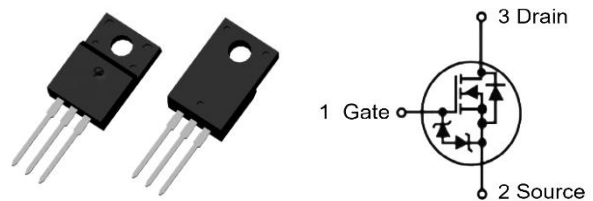
ORDERING INFORMATION

Package Type	Part Number	
TO-220F SPQ: 50pcs/Tube 1,000pcs /Box	T3F	AM80R330T3FVU
Note	V: Halogen free Package U: Tube Package	
AiT provides all RoHS products		

FEATURES

- Fast switching
- Low on resistance
- Low gate charge
- Low reverse transfer capacitances
- 100% single pulse avalanche energy test
- 100% ΔVDS test

PIN DESCRIPTION



Pin#	Symbol	Function
1	G	Gate
2	D	Drain
3	S	Source

**ABSOLUTE MAXIMUM RATINGS**T_C=25°C, unless otherwise Noted

V _{DSS} , Drain-to-Source Voltage		800V
V _{GSS} , Gate-to-Source Voltage		±20V
I _D , Continuous Drain Current	T _C = 25°C	13A
	T _C = 100°C	8.2A
I _{DM} , Pulsed Drain Current ⁽¹⁾		39A
E _{AS} , Single Pulse Avalanche Energy ⁽²⁾		171mJ
I _{AR} , Repetitive Avalanche Current		2A
dv/dt, MOSFET dv/dt ruggedness, V _{DS} =0V~400V		50V/ns
dv/dt, Reverse Diode dv/dt, V _{DS} =0V~400V, I _{DS} ≤I _D		15V/ns
P _{tot} , Power Dissipation	T _a = 25°C	2W
	T _C = 25°C	32W
R _{θJC} , Thermal Resistance, Junction to Case		3.91°C/W
R _{θJA} , Thermal Resistance, Junction to Ambient		62.5°C/W
V _{ISO} , Isolation Voltage		2500V
V _{ESD(G-S)} , Gate-Source ESD (HBM-C=100p, R=1.5KΩ)		2000V
T _{STG} , Storage Temperature Range		-55°C ~ +150°C
T _J , Junction Temperature Range		-55°C ~ +150°C

Stresses above may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the Electrical Characteristics are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

(1) Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

(2) L=10mH, V_{DD}=50V, V_{GS}=10V, R_G=25Ω, V_{GATE}=800V, Start T_J=25°C



ELECTRICAL CHARACTERISTICS

T_C = 25°C, unless otherwise Noted

Parameter	Symbol	Conditions	Min	Typ.	Max	Unit
Off Characteristics						
Drain-to-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = 250μA	800	-	-	V
Drain-to-Source Leakage Current	I _{DSS}	V _{DS} =800V, V _{GS} =0V, T _C = 25°C	-	-	1	μA
		V _{DS} =800V, V _{GS} =0V, T _C = 150°C	-	-	100	
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} = ±20V	-	-	±1	μA
On Characteristics						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} = V _{GS} , I _D = 250μA	2	-	4	V
Drain-to-Source on-state Resistance ⁽¹⁾	R _{DS(ON)}	V _{GS} = 10V, I _D = 3.4A	-	0.33	0.38	Ω
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =500V, V _{GS} =0V, f=1.0MHZ	-	1250	-	pF
Output Capacitance	C _{oss}		-	26	-	
Reverse Transfer Capacitance	C _{rss}		-	3.4	-	
Switching Characteristics						
Turn-On Delay Time	t _{d(on)}	V _{DD} =400V, R _G =25Ω, V _{GS} =10V, I _D = 6.2A	-	29	-	ns
Turn-On Rise Time	t _r		-	21	-	
Turn-Off Delay Time	t _{d(off)}		-	136	-	
Turn-Off Fall Time	t _f		-	17	-	
Total Gate Charge	Q _g	V _{DD} = 640V , I _D = 6.2A V _{GS} = 10V	-	29	-	nC
Gate-Source Charge	Q _{gs}		-	5.3	-	
Gate-Drain Charge	Q _{gd}		-	8.9	-	
Drain-Source Diode Characteristics						
Diode Forward Current	I _s	-	-	-	13	A
Diode Forward Voltage *	V _{FSD}	V _{GS} =0V, I _s =6.2A	-	-	1.20	V
Reverse Recovery Time *	t _{rr}	I _F = 6.2A, T _J = 25°C	-	260	-	ns
Reverse Recovery Charge	Q _{rr}	di/dt = 100A/us	-	2700	-	nC

* Pulse test: Pulse width ≤ 300μs, Duty Cycle ≤ 2%.



TYPICAL PERFORMANCE CHARACTERISTICS

Fig 1. On Region Characteristics

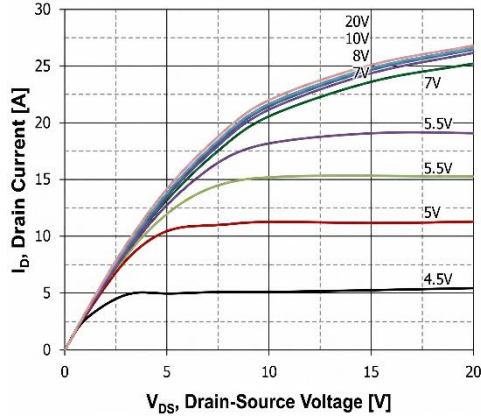


Fig 2. Transfer Characteristics

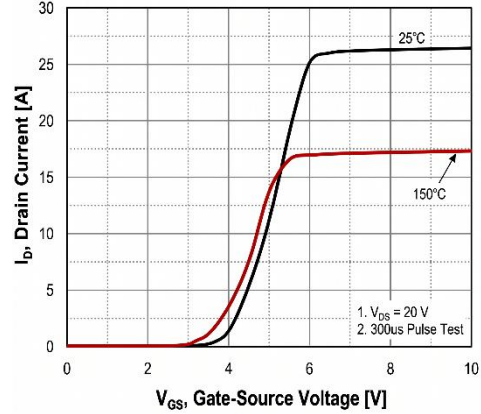


Fig 3. On-Resistance Variation vs. Drain Current and Gate Voltage

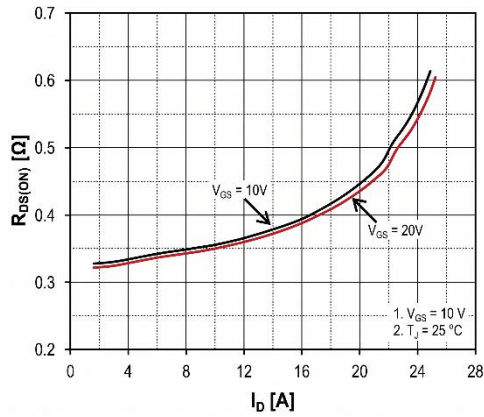


Fig 4. Body Diode Forward Voltage Variation with Source Current and Temperature

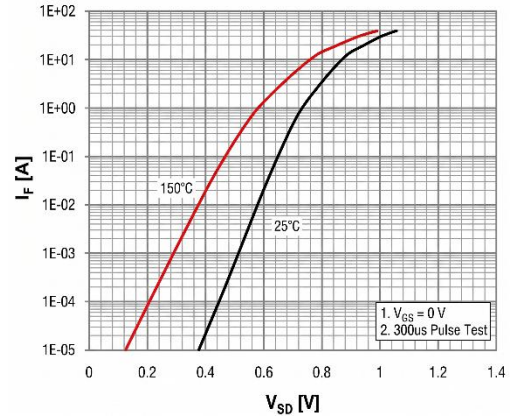


Fig 5. Capacitance Characteristics

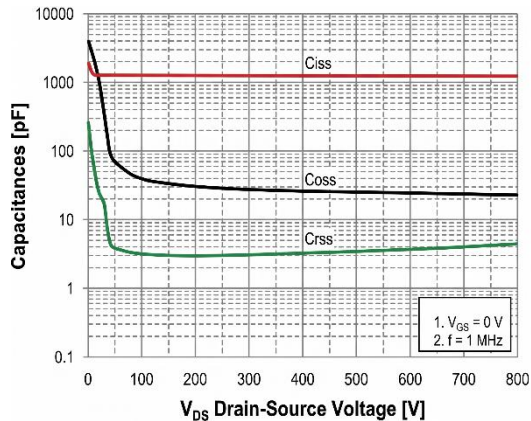


Fig 6. Gate Charge Characteristics

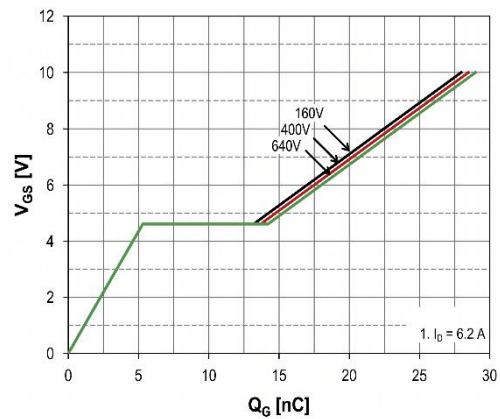




Fig 7. Breakdown Voltage Variation vs. Temperature

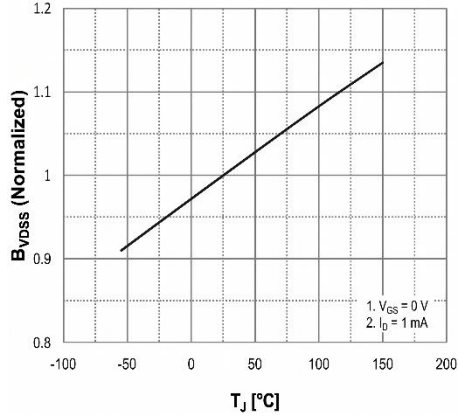


Fig 8. On-Resistance Variation vs. Temperature

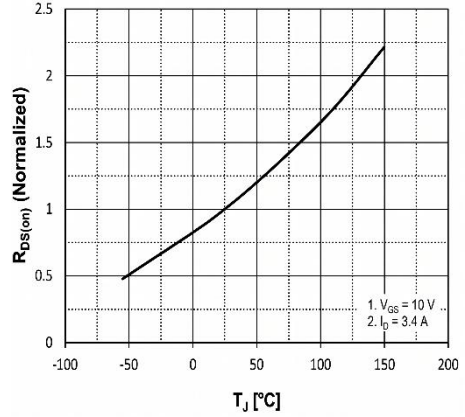


Fig 9. Maximum Safe Operating Area

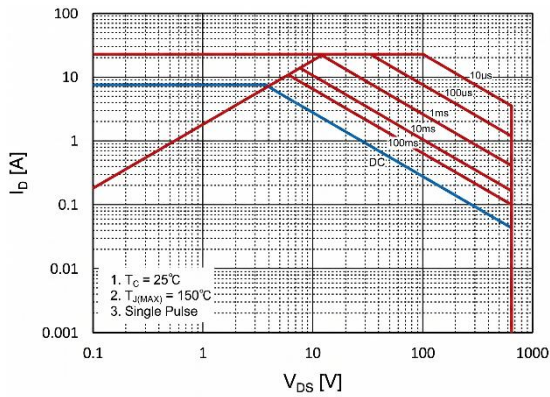


Fig 10. Maximum Drain Current vs. Case Temperature

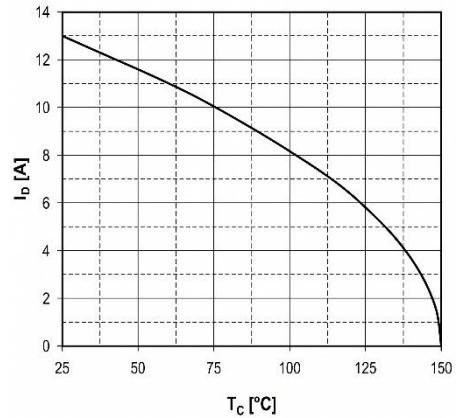


Fig 11. Transient Thermal Response Curve

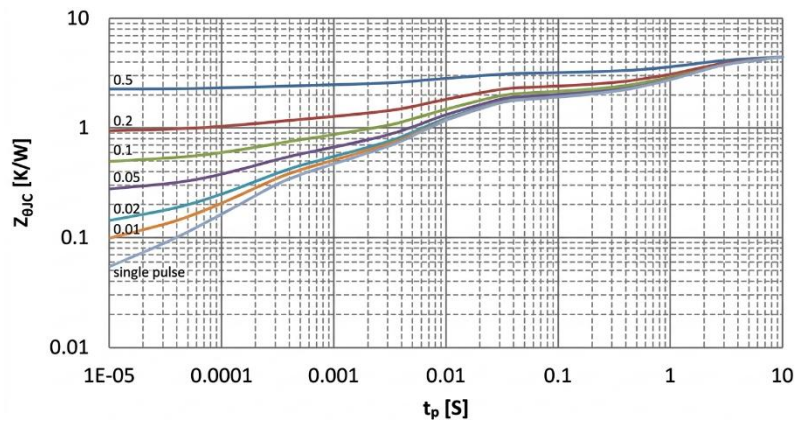




Fig 13. Gate Charge Test Circuit & Waveform

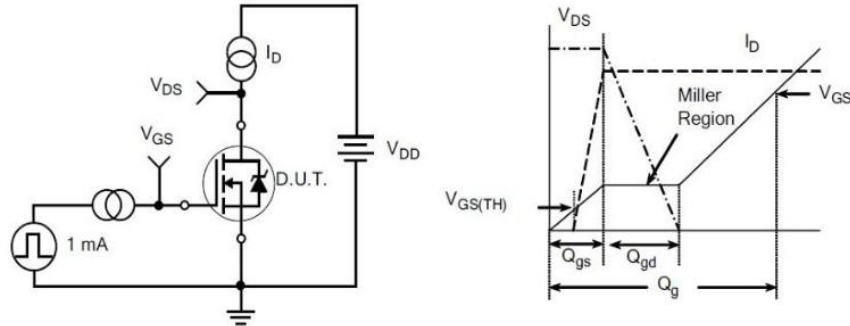


Fig 14. Resistive Switching Test Circuit & Waveform

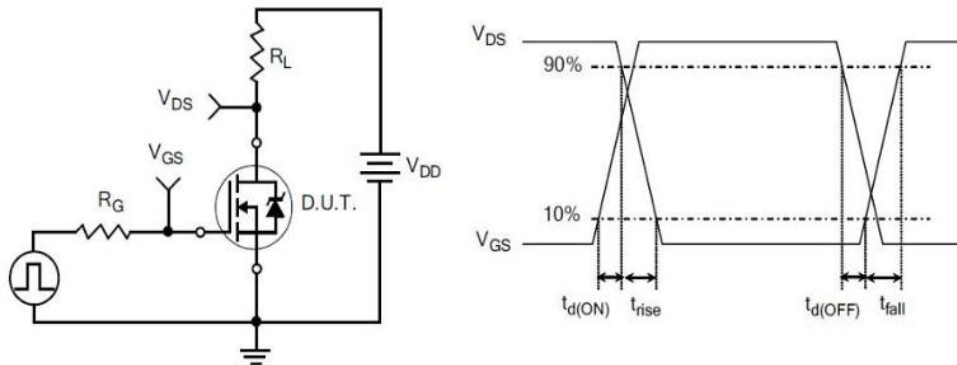


Fig 15. Diode Recovery Test Circuit & Waveform

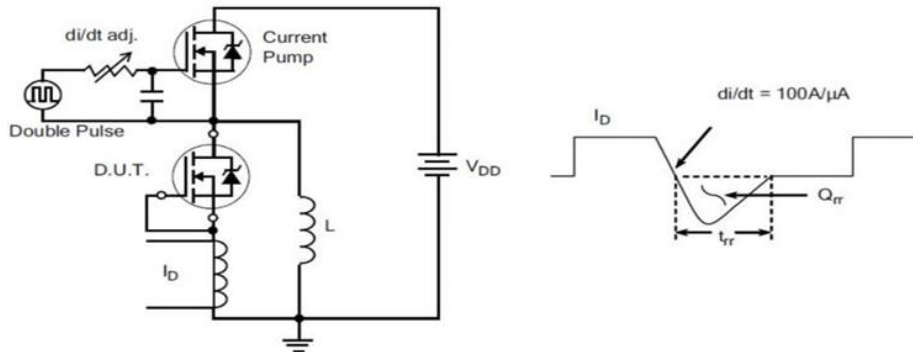
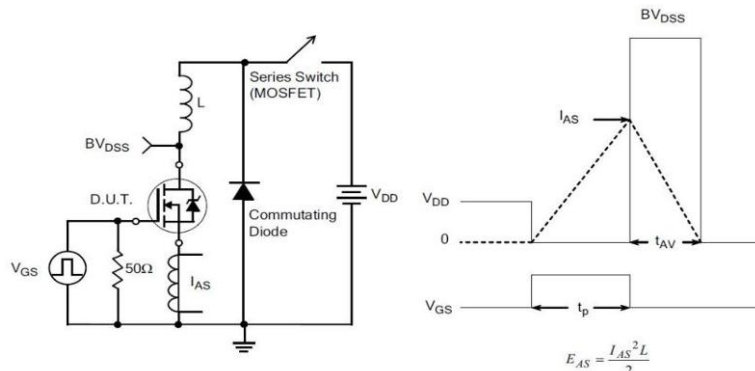


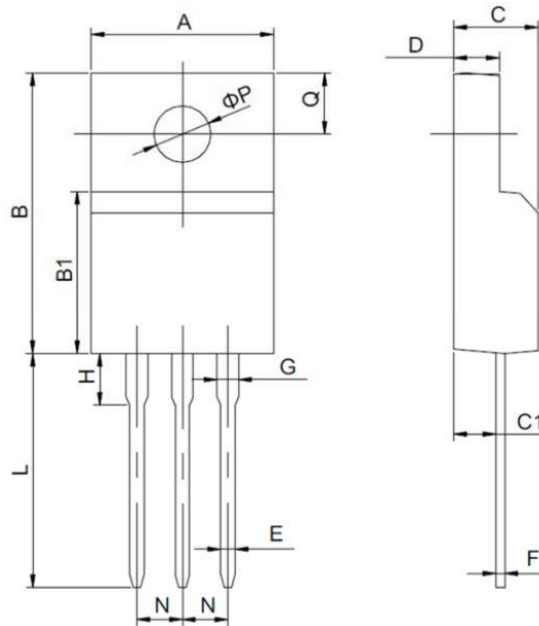
Fig 16. Unclamped Inductive Switching Test Circuit & Waveform





PACKAGE INFORMATION

Dimension in TO-220F (Unit: mm)



Symbol	Min.	Max.
A	9.600	10.400
B	15.400	16.200
B1	8.900	9.500
C	4.300	4.900
C1	2.100	3.000
D	2.400	3.000
E	0.600	1.000
F	0.300	0.600
G	1.120	1.420
H	1.600	3.800
L	12.000	14.000
N	2.340	2.740
Q	3.150	3.550
ΦP	2.900	3.300



IMPORTANT NOTICE

AiT Semiconductor Inc. (AiT) reserves the right to make changes to any its product, specifications, to discontinue any integrated circuit product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

AiT Semiconductor Inc. integrated circuit products are not designed, intended, authorized, or warranted to be suitable for use in life support applications, devices or systems or other critical applications. Use of AiT products in such applications is understood to be fully at the risk of the customer. As used herein may involve potential risks of death, personal injury, or server property, or environmental damage. In order to minimize risks associated with the customer's applications, the customer should provide adequate design and operating safeguards.

AiT Semiconductor Inc. assumes to no liability to customer product design or application support. AiT warrants the performance of its products of the specifications applicable at the time of sale.