



DESCRIPTION

The AM03N100D-Q is available in TO-252 Packages.

VDS	RDS(ON)	ID
1000V	6.2Ω	2.5A

APPLICATIONS

- High frequency switching mode power supply

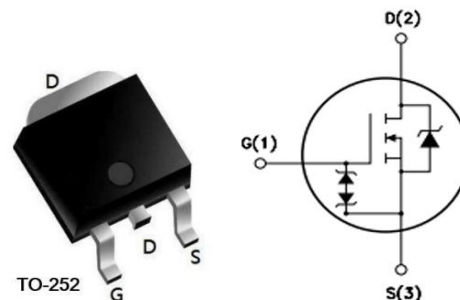
ORDERING INFORMATION

Package Type	Part Number	
TO-252 AEC-Q101 SPQ: 2,500pcs/Reel	D	AM03N100D-Q
Note	Q: AEC-Q101 Qualified	
AiT provides all RoHS products		

FEATURE

- AEC-Q101 Certificated
- VDS = 1000V, ID = 2.5A
- RDS(ON) = 6.2Ω @ VGS=10V
- Fast Switching
- Low Crss
- 100% avalanche tested
- Improved dv/dt capability
- Zener - Protected

PIN DESCRIPTION



Pin #	Symbol	Function
1	G	Gate
2	D	Drain
3	S	Source

**ABSOLUTE MAXIMUM RATINGS**

T_C = 25°C, unless otherwise specified.

V _{GS} , Gate-to-Source Voltage		±30V
V _{DSS} , Drain-to-Source Voltage		1000V
I _D , Drain Continuous-Continuous	T _C =25°C	2.50A
	T _C =100°C	1.58A
I _{DM} ⁽¹⁾ , Pulsed Drain Current		10A
P _D , Power Dissipation	TO-252	62.50W
	Derating Factor above 25°C	0.50W/°C
E _{AS} ⁽²⁾ , Single Pulse Avalanche Energy		200mJ
V _{ESD(G-S)} , Gate source ESD (HBM, C=100pF, R=1.5kΩ)		3kV
dv/dt ⁽³⁾ , Peak Diode Recovery dv/dt		5V/ns
T _{STG} , Storage Temperature Range		-55°C~+150°C
T _J , Operating Junction Temperature Range		150°C
T _L , Maximum Temperature for Soldering		300°C
R _{θJC} , Junction-to-Case	TO-252	2°C/W

Stresses above may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the Electrical Characteristics are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

(1) Repetitive Rating: Pulse width limited by maximum junction temperature.

(2) L=120mH, V_{DS}=50V, Start T_J=25°C.

(3) I_{SD} =3A, di/dt ≤100A/us, V_{DD}≤B_VD_{SS}, Start T_J=25°C

**ELECTRICAL CHARACTERISTICS**T_C = 25°C, unless otherwise specified.

Parameter	Symbol	Conditions	Min	Typ.	Max	Unit
OFF Characteristics						
Drain to Source Breakdown Voltage	V _{DSS}	V _{GS} =0V, I _D =250μA	1000	-	-	V
BV _{DSS} Temperature Coefficient	ΔBV _{DSS} / ΔT _J	I _D =250μA, Reference 25°C	-	0.75	-	V/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =1000V, V _{GS} =0V, T _J = 5°C	-	-	10	μA
		V _{DS} =800V, V _{GS} =0V, T _J = 125°C	-	-	100	
Gate to Source Forward Leakage	I _{GSS(F)}	V _{GS} = 25V	-	-	10	μA
Gate to Source Reverse Leakage	I _{GSS(R)}	V _{GS} = -25V	-	-	10	
ON Characteristics						
Gate Threshold Voltage*	V _{GS(TH)}	V _{GS} =V _{DS} , I _D =250μA	3	-	5	V
Drain-to-Source On-Resistance*	R _{DS(ON)}	V _{GS} =10V, I _D =1.25A	-	6.20	7.50	Ω
Forward Transconductance*	g _{fs}	V _{GS} =15V, I _D =1.25A	2	-	-	S
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1MHz	-	530	-	pF
Output Capacitance	C _{oss}		-	45	-	
Reverse Transfer Capacitance	C _{rss}		-	2.5	-	
Total Gate Charge	Q _g	V _{DD} =450V, I _D =2.5A, V _{GS} =10V	-	13.80	-	nC
Gate-Source Charge	Q _{gs}		-	4.60	-	
Gate-Drain Charge	Q _{gd}		-	4.80	-	
Gate resistance	R _G	f = 1.0MHz	-	4.50	-	Ω
Switching Characteristics						
Turn-on Delay Time	t _{d(on)}	V _{DD} =500V, I _D =2.5A, R _{GEN} =5Ω, V _{GS} =10V, T _J = 25°C	-	23	-	ns
Turn-on Rise Time	t _r		-	63	-	
Turn-Off Delay Time	t _{d(off)}		-	33	-	
Turn-Off Fall Time	t _f		-	61	-	
Drain-Source Diode Characteristics						
Drain Forward Voltage*	V _{SD}	I _S =2.5A, V _{GS} =0V	-	-	1.20	V
Continuous Source Current (Body Diode)	I _S	T _C = 25°C	-	-	2.5	A
Maximum Pulsed Current (Body Diode)	I _{SM}		-	-	10	
Reverse Recovery Time	T _{rr}	I _S =2.5A, T _J = 25°C V _{GS} =0V,	-	2103	-	ns
Reverse Recovery Charge	Q _{rr}	di _F /dt=100A/us	-	1979	-	nC
Gate-source Zener diode						
Gate-Source Breakdown Voltage	V _{GSO}	I _{GS} = ±1mA (Open Drain)	30	-	-	V

* Pulse width t_p ≤ 300μs, δ ≤ 2%



TYPICAL PERFORMANCE CHARACTERISTICS

Fig 1. Safe Operating Area

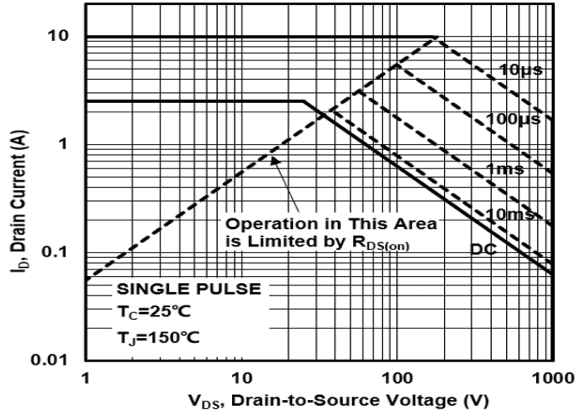


Fig 2. Power Dissipation

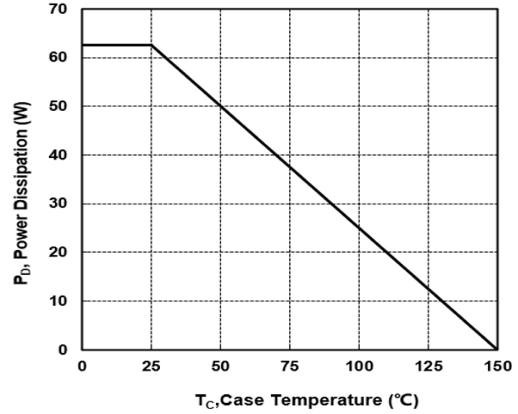
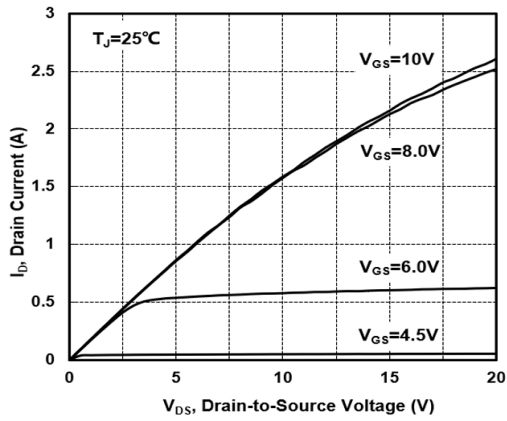


Fig 3. Typical Output Characteristics



a

Fig 4. Typical Transfer Characteristics

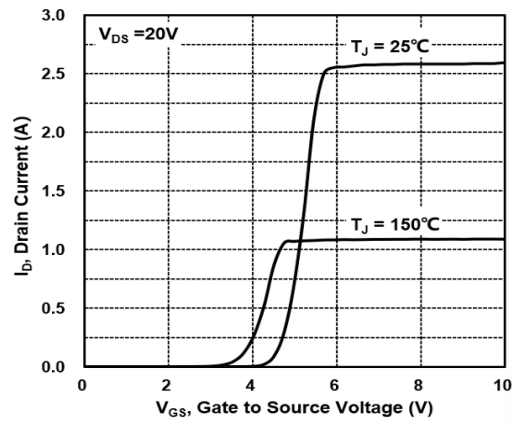


Fig 5. Typical Drain to Source ON Resistance vs. Drain Current

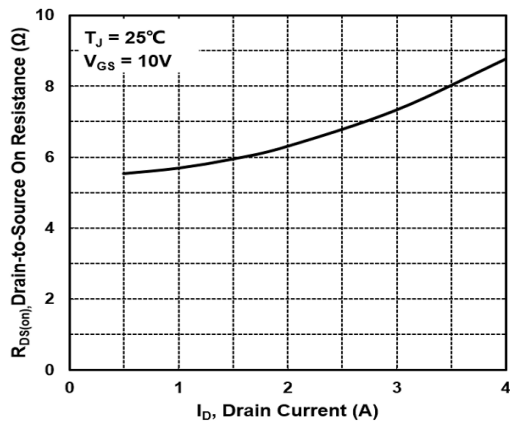


Fig 6. Typical Drain to Source on Resistance vs. Junction Temperature

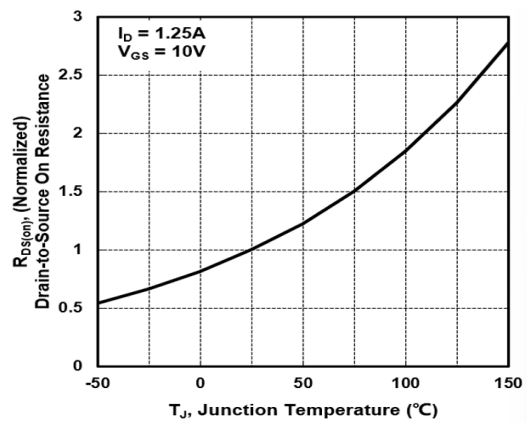




Fig 7. Typical Threshold Voltage vs. Junction Temperature

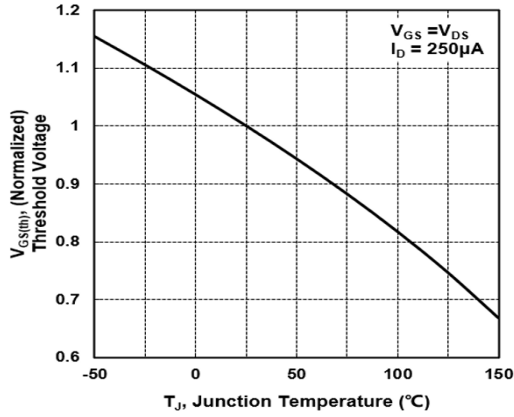


Fig 8. Typical Breakdown Voltage vs. Junction Temperature

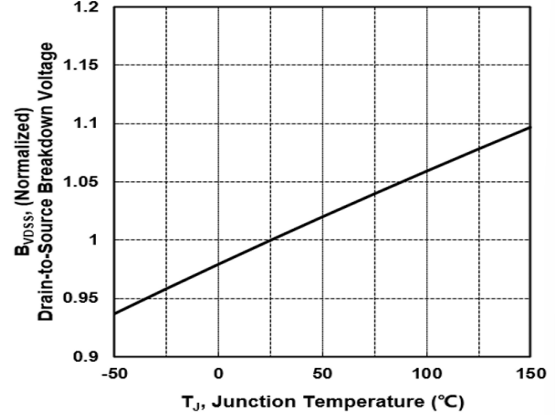


Fig 9. Typical Capacitance vs Drain to Source Voltage

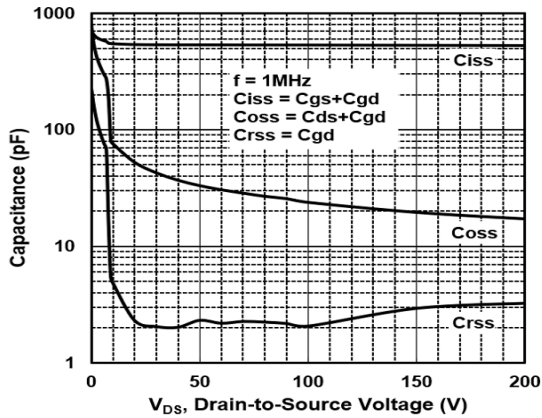


Fig 10. Typical Gate Charge vs Gate to Source Voltage

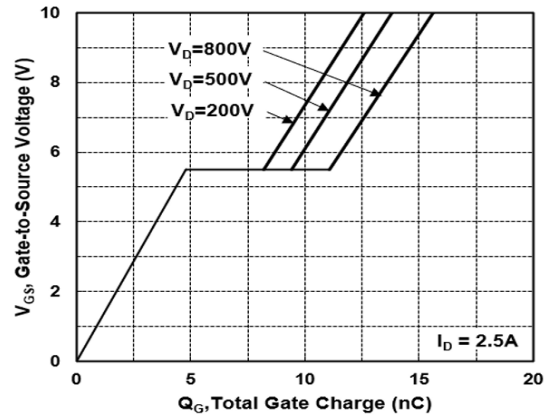


Fig 11. Transient Thermal Impedance, Junction to Case

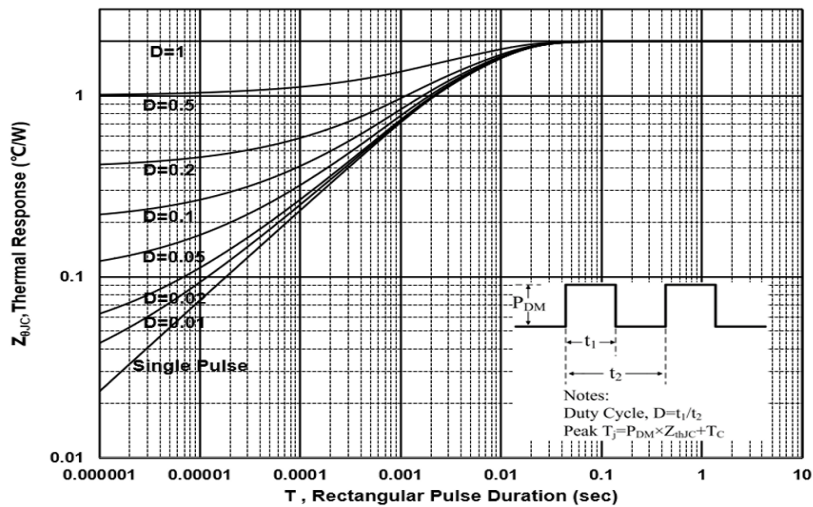




Fig 12. Gate Charge Test Circuit

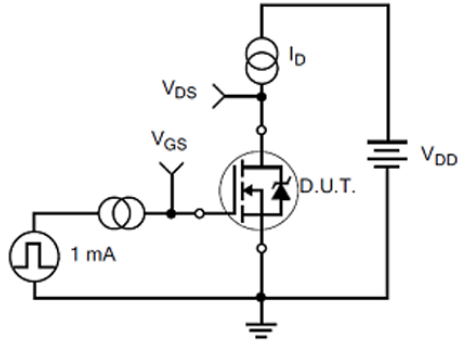


Fig 13. Gate Charge Waveforms

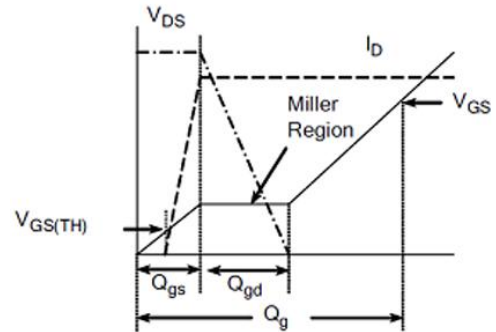


Fig 14. Resistive Switching Test Circuit

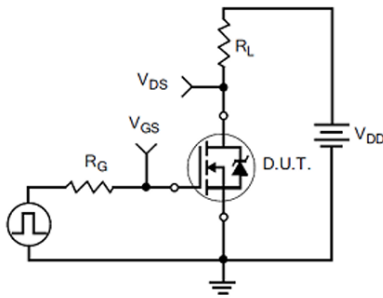


Fig 15. Resistive Switching Waveforms

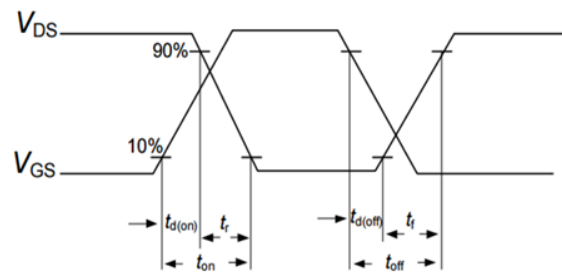


Fig 16. Diode Reverse Recovery Test Circuit

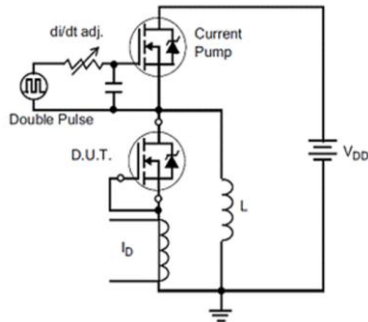


Fig 17. Diode Reverse Recovery Waveform

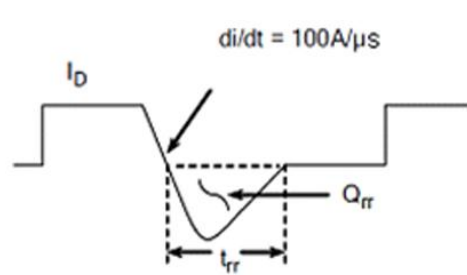


Fig 18. Unclamped Inductive Switching Test Circuit

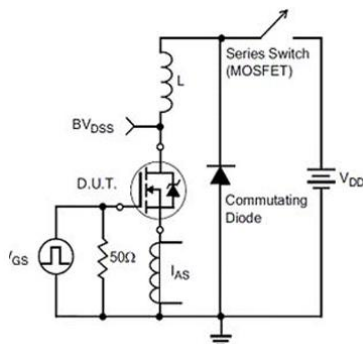
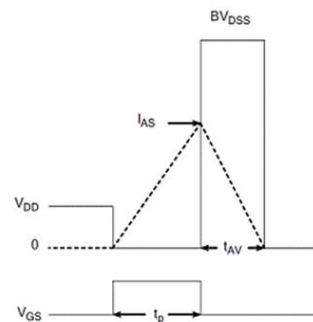


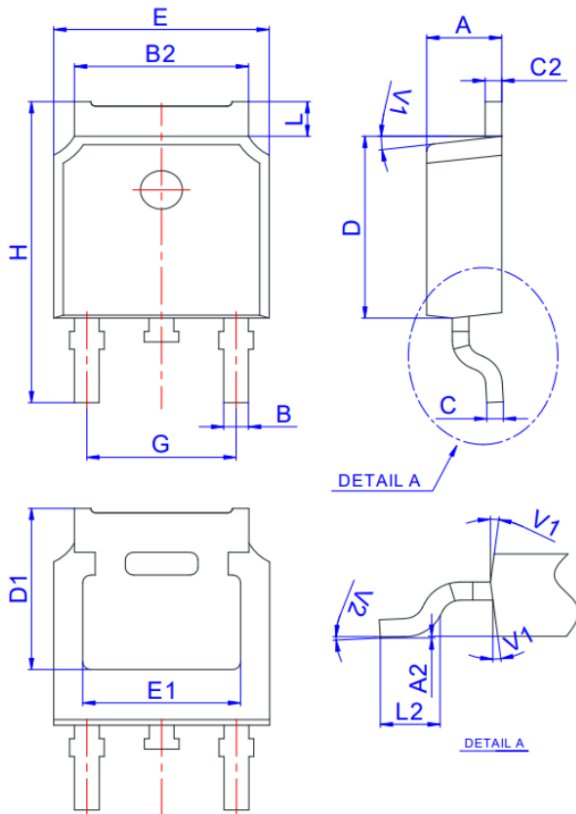
Fig 19. Unclamped Inductive Switching Waveform





PACKAGE INFORMATION

Dimension in TO-252 (Unit: mm)



Symbol	Min.	Max.
A	2.100	2.500
A2	0.000	0.100
B	0.660	0.860
B2	5.180	5.480
C	0.400	0.600
C2	0.440	0.580
D	5.900	6.300
D1	5.300REF	
E	6.400	6.800
E1	4.630	-
G	4.470	4.670
H	9.500	10.70
L	1.090	1.210
L2	1.350	1.650
V1	7°	
V2	0°	6°



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